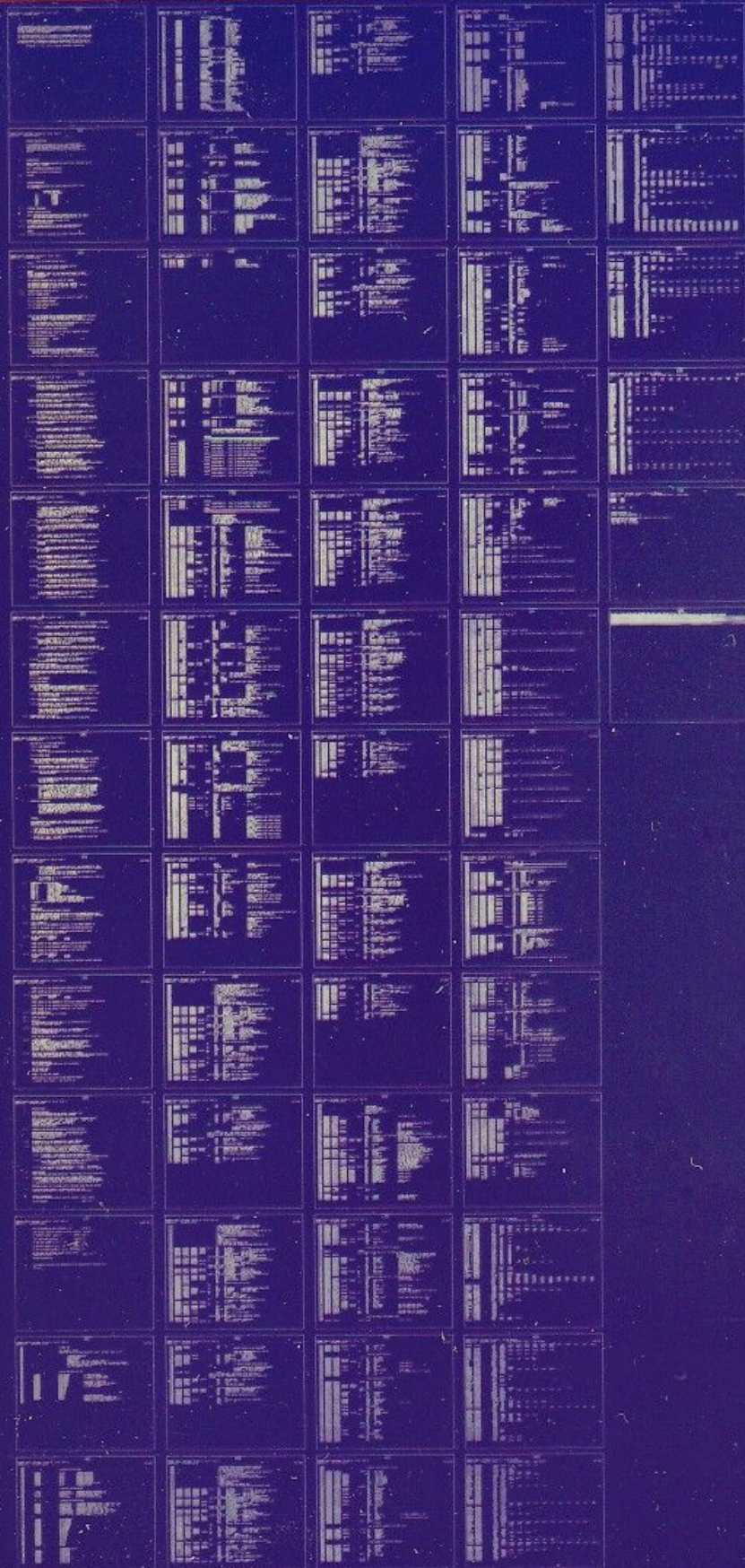


# DU11

OFF-LINE COMBINED TEST  
MD-11-DZDUF-C

EP-DZDUF-C-DL-A  
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FICHE 1 OF 1

NOV 1976  
  
MADE IN U.S.A.



Frame	Test Data
1	TEST RESULTS
2	STATUS: PASS
3	PARAMETER: 100
4	MEASUREMENT: 50.0
5	UNIT: VOLTS
6	DATE: 11/1976
7	TIME: 14:30
8	OPERATOR: J.D.
9	TEST ID: MD-11-DZDUF-C
10	VERSION: 1.0
11	DESCRIPTION: OFF-LINE TEST
12	RESULTS: ALL OK
13	REMARKS:
14	TESTER: J.D.
15	APPROVER: J.D.
16	TEST DATE: 11/1976
17	TEST TIME: 14:30
18	TEST LOCATION: LAB 1
19	TEST EQUIPMENT: MD-11
20	TEST PART: DZDUF-C

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## GENERAL DESCRIPTION

THIS DIAGNOSTIC CAN CHAIN 16 D11'S. THIS MEANS THAT 16 DEVICES CAN BE SEQUENTIALLY EXERCISED. THE DIAGNOSTIC MAKES ONE PASS BEFORE PROCEEDING TO THE NEXT DEVICE, AND CONTINUES EXERCISING ALL DEVICES IN THIS FASHION UNTIL HALTED.

## 2. REQUIREMENTS

PDP-11 FAMILY STANDARD COMPUTER WITH OR WITHOUT HARDWARE SWITCH REGISTER (LOC. 177570)

D11 SYNCHRONOUS/ISOCRONOUS OPTION

ONE CONSOLE TELETYPE OR EQUIVALENT

## 2.2 STORAGE

## 3. LOADING PROCEDURE

THE STANDARD PROCEDURE FOR LOADING ABSOLUTE BINARY TAPES IS TO BE USED.

	STARTING ADDRESS FOR ABSOLUTE LOADER
4K	017500
8K	037500
12K	057500
16K	077500
20K	117500
24K	137500
28K	157500

## 4. STARTING PROCEDURE

## 4.1 CONTROL SWITCH SETTINGS

NOTE: SOFTWARE SWITCH REGISTER IS DEFINED AS LOC. 176, WHILE THE SOFTWARE DISPLAY REGISTER IS DEFINED AS LOC. 174.

4.1.1 AFTER PROGRAM LOAD (INITIAL PROGRAM START)  
ALL CONSOLE SWITCHES DOWN4.1.2 TO MODIFY DEVICE VECTOR AND CONTROL REGISTER ADDRESSES  
AFTER PROGRAM RESTART OR TO RUN MULTIPLE DEVICES

SMDD=1

## 4.1.3 TO START PROGRAM AT SELECTED TEST AFTER A PROGRAM RESTART

(ONLY IN SINGLE DEVICE TESTS)  
SM01=1

- 4.1.4 TO LOCK ON SELECTED TEST AFTER A PROGRAM RESTART  
(ONLY IN SINGLE DEVICE TESTS)

SM02=1  
NOTE1: IN GENERAL SM01 WILL BE USED WHEN SM02=1 IS USED  
NOTE2: WITHOUT SM01=1 "LOCK ON TEST" WILL DEFAULT TO TEST 1  
STARTING ADDRESS

4.2

THE STARTING ADDRESS FOR ALL TESTS IS 000200

THE RETARTING ADDRESS FOR ALL TESTS IS 000200  
THE STARTING ADDRESS TO ENTER A SELECTED TEST IS 000200  
THE STARTING ADDRESS TO LOCK ON TEST IS 000200

- 4.3 PROGRAM AND/OR OPERATOR ACTION

- 4.3.1 INITIAL PROGRAM START

4.3.1.1 LOAD PROGRAM INTO MEMORY WITH ABSOLUTE LOADER

4.3.1.2 LOAD ADDRESS 000200

4.3.1.3 CLEAR CONSOLE SWITCHES

4.3.1.4 PRESS START

NOTE: IF THE SOFTWARE SWITCH REGISTER IS SELECTED THEN THE FOLLOWING  
WILL BE TYPED AFTER THE PROGRAM IDENTIFIES ITSELF:  
SMR=XXXXXX MEM= (REFER TO SECTION 5. FOR OPERATOR'S OPTION)

4.3.1.7 THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT IS ABOUT  
TO START TESTING ,AND THEN TESTING WILL BEGIN

- 4.3.2 PROGRAM RESTART WITH ALL SWITCHES DOWN

4.3.2.1 THE PROGRAM WILL TYPE "R" AND WILL COMMENCE TESTING

- 4.3.3 PROGRAM RESTART WITH SM00=1

4.3.3.1 LOAD ADDRESS 000200

4.3.3.2 SET SM00=1

4.3.3.3 PRESS START

NOTE: IF THE SOFTWARE SWITCH REGISTER IS SELECTED THEN THE FOLLOWING  
WILL BE TYPED AFTER THE PROGRAM IDENTIFIES ITSELF:  
SMR=XXXXXX MEM= (REFER TO SECTION 5. FOR OPERATOR'S OPTION)

4.3.3.4 THE PROGRAM WILL TYPE " 1ST DEVICE: RECEIVER CONTROL REGISTER

ADDRESS" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.5 TYPE IN THE ADDRESS OF THE FIRST RECEIVER CONTROL REGISTER ADDRESS OF THE DUII TO BE TESTED FOLLOWED BY A <CARRIAGE RETURN>

IF AN INCORRECT ADDRESS IS TYPED ,THE PROGRAM WILL TYPE "?" AND WILL THEN REPEAT THE MESSAGE OF 4.3.3.4

4.3.3.6 THE PROGRAM WILL TYPE "VECTOR ADDRESS-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.7 TYPE IN THE BASE RECEIVER INTERRUPT VECTOR ADDRESS FOR THE DUII TO BE TESTED FOLLOWED BY A <CARRIAGE RETURN>

IF AN INCORRECT ADDRESS IS TYPED ,THE PROGRAM WILL TYPE "?" AND WILL THEN REPEAT THE MESSAGE OF 4.3.3.6

4.3.3.8 THE PROGRAM WILL TYPE "ARE YOU RUNNING MULTIPLE DEVICES ?" (Y OR N)-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.9 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED BY A <CARRIAGE RETURN>

IF AN INCORRECT ANSWER IS GIVEN, THE PROGRAM WILL TYPE "?" AND WILL THEN REPEAT THE MESSAGE OF 4.3.3.8

IF A "NO" ANSWER IS GIVEN: JUMP TO SECTION 4.3.3.12  
IF A "YES" ANSWER IS GIVEN:THE NEXT QUESTION IS ASKED

4.3.3.10 THE PROGRAM WILL TYPE "LAST DEVICE:RECEIVER CONTROL REGISTER ADDRESS-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.11 TYPE IN THE ADDRESS OF THE LAST RECEIVER CONTROL REGISTER ADDRESS OF THE DUII TO BE TESTED FOLLOWED BY A <CARRIAGE RETURN>

IF AN INCORRECT ANSWER IS TYPED ,THE PROGRAM WILL TYPE "?" AND WILL THEN REPEAT THE MESSAGE OF 4.3.3.10  
NOTE:ALL ADDRESSES SHALL BE CONTIGUOUS

4.3.3.11.1 IF AN "OUT OF RANGE" ADDRESS IS TYPED IE. MORE THAN 16 (10) DEVICES AWAY (UPWARDS).....THE PROGRAM WILL TYPE "OUT OF RANGE:RETYPE LAST DEVICE RXCSR ADDRESS-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.11.2 TYPE IN THE ADDRESS OF THE LAST RECEIVER CONTROL REGISTER ADDRESS OF THE DUII TO BE TESTED FOLLOWED BY A <CARRIAGE RETURN>

IF AN INCORRECT ANSWER IS TYPED ,THE PROGRAM WILL TYPE "?"

AND WILL REPEAT THE MESSAGE OF 4.3.3.11.1

IF A DEVICE ADDRESS LOWER THAN 1ST DEVICE ADDRESS IS TYPED.....  
SCHOOLS OUT..... THERE IS NO PROTECTION FOR THIS.  
THE PROGRAM WILL DEFAULT TO TWO DEVICES ACTIVE (UPWARDS FROM  
1ST DEVICE ADDRESS). THE SAME APPLIES TO IDENTICAL ADDRESSES  
TYPED FOR FIRST AND LAST DEVICE.  
OBSERVE LOCATION 2 ACTREG: SEE SECTION 7.2

4.3.3.12 THE PROGRAM WILL TYPE "DU PRIORITY LEVEL-" AND  
WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.13 TYPE IN THE APPROPRIATE DEVICE PRIORITY LEVEL OF THE  
DU11 OR DU11'S TO BE TESTED FOLLOWED BY A <CARRIAGE RETURN>  
(NOTE THAT ALL MULTIPLE DEVICES MUST BE AT THE SAME PRIORITY  
LEVEL). IE "5"

IF AN INCORRECT LEVEL IS TYPED, THE PROGRAM WILL TYPE "?"  
AND REPEAT THE MESSAGE OF 4.3.3.12

4.3.3.14 THE PROGRAM WILL TYPE "# OF SYNC CHARS  
SELECTED (1 OR 2)-" AND WAIT FOR AN INPUT FROM THE TELETYPE  
KEYBOARD

4.3.3.15 TYPE IN THE APPROPRIATE ANSWER "1" OR "2" FOLLOWED  
BY A <CARRIAGE RETURN>. (NOTE: ALL MULTIPLE DEVICES MUST  
BE THE SAME)

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?"  
AND WILL REPEAT THE MESSAGE OF 4.3.3.14

4.3.3.16 THE PROGRAM WILL TYPE "IS SEC XMIT JUMPER 86 IN ? (Y OR N)-"  
AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.17 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED  
BY A <CARRIAGE RETURN>. (NOTE THAT ALL MULTIPLE DEVICES  
MUST BE THE SAME)

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?"  
AND WILL REPEAT THE MESSAGE OF 4.3.3.16

4.3.3.18 THE PROGRAM WILL TYPE "IS SEC REC JUMPER # 5 IN ?  
(Y OR N)-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.19 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED  
BY A <CARRIAGE RETURN>. (NOTE: ALL MULTIPLE DEVICES MUST BE THE SAME)

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?"  
AND WILL REPEAT THE MESSAGE OF 4.3.3.18

4.3.3.20 THE PROGRAM WILL TYPE "IS OPT CLR ENABLE JUMPER

8 4 IN ? (Y OR N)-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.21 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED BY A <CARRIAGE RETURN>. (NOTE: ALL MULTIPLE DEVICES MUST BE THE SAME)

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?" AND WILL REPEAT THE MESSAGE OF 4.3.3.20

4.3.3.22 THE PROGRAM WILL TYPE "ARE YOU RUNNING IN MAINT. MODE EXTERNAL ? AND ..... DO YOU HAVE THE EXTERNAL MODEM BYPASS JUMPER CONNECTOR ON ? (Y OR N)-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.23 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED BY A <CARRIAGE RETURN>. (NOTE: ALL MULTIPLE DEVICES MUST BE THE SAME)

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?" AND WILL REPEAT THE MESSAGE OF 4.3.3.22

4.3.3.24 THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT HAS STARTED AND WILL COMMENCE TESTING AT TEST 1

4.3.4 PROGRAM RESTART WITH SMD1=1  
NOTE: THIS WILL ONLY WORK WHEN A SINGLE DEVICE IS SELECTED  
,,,IT WILL NOT WORK IF MULTIPLE DEVICES ARE SELECTED  
IF MULTIPLE DEVICES WERE PREVIOUSLY SELECTED, LOAD 000200,  
AND SELECT SMD0=1 AND ANSWER "NO" TO THE MULTIPLE DEVICE QUESTION  
SEE 4.3.3

4.3.4.1 LOAD 000200

4.3.4.2 SET SMD1=1

4.3.4.3 PRESS START  
NOTE: IF THE SOFTWARE SWITCH REGISTER IS SELECTED THEN THE FOLLOWING  
WILL BE TYPED AFTER THE PROGRAM IDENTIFIES ITSELF:  
SWR=XXXXXX NEW= (REFER TO SECTION 5. FOR OPERATOR'S OPTION)

4.3.4.4 THE PROGRAM WILL TYPE "TEST PC-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.4.5 TYPE IN THE ADDRESS OF THE TEST AT WHICH THE PROGRAM IS TO BE STARTED FOLLOWED BY A <CARRIAGE RETURN>

4.3.4.6 THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT HAS STARTED TESTING AT THE SELECTED TEST

NOTE: CARE MUST BE TAKEN WHEN THIS FEATURE IS USED  
SINCE THERE IS NO PROTECTION AGAINST SELECTING AN ADDRESS THAT IS IN THE MIDDLE OF A TEST

4.3.5 PROGRAM RESTART WITH SMD2 =1  
NOTE: THIS WILL ONLY WORK WHEN A SINGLE DEVICE IS SELECTED

SEE NOTE IN 4.3.4 FOR MORE DETAILS

4.3.5.1 LOAD ADDRESS 000200

4.3.5.2 SET SWD2 =1

NOTE: IT MAY BE ADVANTAGEOUS TO SET SWD1=1 (OPTIONAL)

4.3.5.3 PRESS START

NOTE: IF THE SOFTWARE SWITCH REGISTER IS SELECTED THEN THE FOLLOWING WILL BE TYPED AFTER THE PROGRAM IDENTIFIES ITSELF:

SWR=XXXXXX NEW= (REFER TO SECTION 5. FOR OPERATOR'S OPTION)

4.3.5.4 THE PROGRAM WILL TYPE "LOCK ON SELECTED TEST ? (Y OR N)-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.5.5 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED BY A <CARRIAGE RETURN>

IF A NO ANSWER IS GIVEN: THIS LOCK ON TEST WILL BE IGNORED AND THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT HAS STARTED TESTING AT TEST 1

4.3.5.6 IF A YES ANSWER WAS GIVEN: THE PROGRAM WILL ACT AS FOLLOWS... THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT HAS STARTED TESTING AT TEST 1 AND WILL REMAIN IN TEST 1 UNTIL HALTED OR IF ANY KEY IS STRUCK ON THE TELETYPE THE PROGRAM WILL FREEZE ON THE NEXT TEST UNTIL A KEY IS STRUCK ON THE TELETYPE AND SO FORTH THRU THE PROGRAM. IF SWD1 =1 IT WILL PERFORM AS IN SECTION 4.3.4 ALLOWING ONE TO FREEZE ON A SELECTED TEST RATHER THAN DEFAULTING TO TEST 1

## 5. OPERATING PROCEDURE

IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH ALLOWS THE USER THE SAME SWITCH OPTIONS AS THE HARDWARE SWITCH REGISTER. IF THE HARDWARE SWITCH REGISTER DOES NOT EXIST OR IF ONE DOES AND IT CONTAINS ALL ONES (177777) THEN THE SOFTWARE SWITCH REGISTER (LOC. 176) IS USED.

### CONTROL:

THIS PROGRAM ALSO SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE SWITCH REGISTER (LOC. 176) FROM THE TTY. THIS CAN BE ACCOMPLISHED BY DOING THE FOLLOWING:

- 1) TYPE CONTROL G (<IG>); THIS WILL ALLOW THE TTY TO ENTER DATA INTO LOC. 176 AT SELECTED POINTS WITHIN THE PROGRAM.
- 2) THE MACHINE WILL THEN TYPE: SWR=XXXXXXNEW= (XXXXXX IS THE OCTAL CONTENTS OF THE SOFTWARE SWITCH REGISTER.)
- 3) AFTER THE "NEW=" HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE OF THE FOLLOWING AT THE TTY:



- A) TYPE A NUMBER TO BE LOADED INTO LOC. 176 FOLLOWED BY A <CR>. (ONLY NUMBERS BETWEEN 0-7 WILL BE ACCEPTED AND ONLY 6 NUMBERS WILL BE ALLOWED)  
IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH REGISTER CONTENTS WILL NOT BE CHANGED.
- B) IF A CONTROL U <IU> IS DEPRESSED THEN THE PROGRAM WILL SEND YOU BACK TO STEP 2.

5.1 OPERATIONAL SWITCH SETTINGS

SW15 =1	HALT ON ERROR
SW14 =1	LOOP ON CURRENT TEST
SW13 =1	INHIBIT ERROR TYPEOUT
SW11 =1	INHIBIT ITERATIONS
SW10 =1	ESCAPE TO NEXT TEST ON ERROR
SW08 =1	LOOP ON ERROR
SW02 =1	LOCK ON TEST
SW01 =1	RESTART PROGRAM AT SELECTED TEST
SW00 =1	RESELECT VECTOR AND CONTROL REGISTER ADDRESSES & PARAMETERS AFTER A PROGRAM RESTART

TO INHIBIT "END OF PASS" TYPEOUT - TURN TELETYPE OFF

## 6. ERRORS

### 6.1 ERROR HALTS THERE ARE FOUR DISTINCT ERROR TYPEOUTS

NOTE: IF THE SOFTWARE SWITCH REGISTER IS TO BE CHANGED AFTER A HALT THE THE OPERATOR IS REQUIRED TO TYPE A <IG> BEFORE DEPRESSING CONTINUE.  
THE FOLLOWING WILL BE TYPED:  
SWR=XXXXXX NEW= (REFER TO SECTION 5. FOR OPERATOR OPTION)

6.1.1 PC+2 = ERROR PC  
WHERE PC +2 IS THE ADDRESS OF THE CALL TO THE ERROR HANDLER +2  
REFER TO THE ABOVE "HLT" IN DIAGNOSTIC FOR ERROR DESCRIPTION  
CHECK ADDRESS 3 RXCSR; TO LOCATE THE DEVICE PRESENTLY UNDER TEST WHEN RUNNING MULTIPLE DEVICES

6.1.2 PC +2 = REGISTER ERROR PC

REGISTER	EXPECTED	ACTUAL
16XXXX	YYYYYY	ZZZZZZ

WHERE 16XXXX IS THE ADDRESS OF THE FAILING DEVICE REGISTER  
WHERE YYYYYY IS THE EXPECTED CONTENTS OF THAT REGISTER  
WHERE ZZZZZZ IS THE ACTUAL CONTENTS OF THAT REGISTER

6.1.3 PC +2 = RECEIVER ERROR PC

REGISTER	EXPECTED	ACTUAL
16XXXX	YYYYYY	ZZZZZZ

WHERE 16XXXX IS THE ADDRESS OF THE FAILING RECEIVER (RXDBUF) REGISTER

WHERE YYYYYY IS THE EXPECTED DATA CONTENTS OF THAT REGISTER

WHERE ZZZZZZ IS THE ACTUAL DATA CONTENTS OF THAT REGISTER

6.1.4 PC +2 = TRANSMITTER ERROR PC REGISTER  
 REGISTER                      EXPECTED                      ACTUAL  
 16XXXX                      YYYYYY                      ZZZZZZ

WHERE 16XXXX IS THE ADDRESS OF THE FAILING TRANSMITTER (TXCSR) REGISTER

WHERE YYYYYY IS THE EXPECTED CONTENTS OF THAT REGISTER

WHERE ZZZZZZ IS THE ACTUAL CONTENTS OF THAT REGISTER

6.1.5 ERROR DESCRIPTIONS  
 SEE LISTINGS FOR DETAILS OF ERRORS

6.2 ERROR RECOVERY

6.2.1 SW15 =0  
 IF THE PROGRAM IS RUN WITH SW15 =0 ,NO OPERATOR ACTION IS REQUIRED TO CONTINUE TESTING

6.2.2 SW15 =1  
 IF THE PROGRAM IS RUN WITH SW15 =1 ,TO CONTINUE TESTING AFTER THE PROGRAM HAS HALTED ,PRESS THE PROCESSOR CONSOLE "CONTINUE SWITCH"

NOTE: THE PC + 2 OF THE "HLT" WILL BE DISPLAYED IN THE DATA LIGHTS

6.2.3 ILLEGAL INTERRUPTS  
 IF AN INTERRUPT OCCURS TO A VECTOR ADDRESS NOT SELECTED DURING PROGRAM INITIALIZATION, THE PROGRAM WILL HALT IN THE TRAP CATCHER. THE ADDRESS AT WHICH THE PROGRAM HALTS IS 2 GREATER THAN THE ADDRESS TO WHICH THE INTERRUPT OCCURED. THE PROGRAM MUST BE RESTARTED AT 000200 TO RECOVER FROM THIS ERROR.

6.2.4 ADDITIONAL TROUBLESHOOTING AIDS ERRCNT: & PASCNT:  
 CHECK THESE TWO TAG LOCATIONS FOR TOTAL # OF ERRORS AND PASSES RESPECTIVELY. LOADING 000200 AND RESTARTING WILL CLEAR THESE LOCATIONS.

6.3 END OF PASS ROUTINE  
 THIS TYPEOUT IS MENTIONED HERE FOR CONVENIENCE IT IS IN THE FORM:

END OF PASS TAPE Y  
 16XXXX = DEVICE

WHERE Y IS THE TAPE LOADED

WHERE 16XXXX IS THE DEVICE'S BASE REGISTER ADDRESS

TO INHIBIT THIS TYPEOUT - TURN TELETYPE OFF

## 7. RESTRICTIONS

## 7.1 MULTIPLE DEVICES

UP TO 16(10) DEVICES MAY BE TESTED. HOWEVER, THEY  
MUST HAVE CONTIGUOUS ADDRESSES AND VECTORS

NOTE: IF ALL DEVICES UNDER TEST HAVE THE SAME INTERRUPT VECTOR  
YOU CAN CHANGE "ZERO: ADD #10, BASEIV ;NEXT BLOCK  
(VECTORS)" TO "ZERO: ADD #0, BASEIV";  
THEREBY THE VECTOR ADDRESSES WILL NOT BE  
UPDATED AFTER EACH PASS.

## 7.2 DISQUALIFYING DEVICES WHEN RUNNING MULTIPLE DEVICES

WHEN RUNNING MULTIPLE DEVICES AN ACTIVE BIT IS SET  
FOR EACH DEVICE RUNNING UNDER TEST IE. BIT 0 FOR  
DEVICE 0 BIT 15 FOR DEVICE 15  
TO DISQUALIFY DEVICES:

7.2.1 IF DEVICE 0 IS TO BE DISQUALIFIED, SIMPLY RESTART  
PROGRAM WITH SWDD =1 AND OMIT THE FIRST DEVICE.

7.2.2 IF HOWEVER, DEVICES 1 THRU 15 OR ANY COMBINATION THEREOF  
ARE TO BE DISQUALIFIED...LOAD THE LOCATION OF ACTREG:  
OBSERVE THE ACTIVE BITS (ACTIVE =1, NONACTIVE = 0)  
AND DEPOSIT 0 WHERE THOSE DEVICES ARE TO BE DISQUALIFIED

7.2.2.1 TO RESTART...LOAD 000200 IN SWR AND DEPRESS START....  
THE PROGRAM WILL CONTINUE WITH THE DEVICE IT WAS IN BEFORE HALTING.

7.2.2.2 .....OR .....LOAD 000200 WITH SWDD =1 AND DEPRESS START....  
ANSWER THE QUESTION :1ST DEVICE : ETC.....  
.....THE PROGRAM WILL CONTINUE WITH DEVICE 0

7.2.2.3 IF ALL DEVICES ARE DISQUALIFIED BY MISTAKE THE PROGRAM  
WILL TYPEOUT AN ERROR MESSAGE.....LOAD & START AT 000200

## 7.3 CABLE DELAYS

NOTE: EXTERNAL LOOP BACK TESTS ONLY (MODEM CABLE WITH H315 CONNECTOR ON)

7.3.1 TO PROVIDE SUFFICIENT DELAY FOR CLOCK SIGNAL OVER THE CABLE,  
LOCATION "HOLD:" MUST BE MODIFIED TO ACCOMODATE FOR FASTER MACHINES.  
PRESENTLY "HOLD:" =20 IS SUFFICIENT TIME ON AN 11/20 MACHINE.  
IF RUNNING ON AN 11/40 OR AN 11/45 "HOLD:" MUST BE PATCHED TO 40

BASICALLY DON'T TRY TO EXCEED 10K TO 12K RATE USING THE EIA DRIVERS

7.4 TO USE THE "XOR" TESTER THE BRANCH AROUND THE "XOR"  
CODE MUST BE PATCHED TO A "NOP". (SEE LISTINGS FOR DETAILS)

## 8.

DEFAULT PARAMETERS:

1ST DEVICE: RECEIVER CONTROL REGISTER ADDRESS- RXCSR: 160040

VECTOR ADDRESS-

DURIV: 770

L01

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 11  
HELLO.P11 03-AUG-76 00:00

SEQ 0011

ARE YOU RUNNING MULTIPLE DEVICES ?- NO           MULTD: 0  
LAST DEVICE: RECEIVER CONTROL REGISTER ADDRESS- LASTADD: 0  
DU PRIORITY LEVEL- LEVEL 5                   DUPRT: LEVEL 5  
# OF SYNC CHARS SELECTED - 2                SYNCNO: 377  
IS SEC XMIT JUMPER # 6 IN ?- YES           SEXMIT: 377  
IS SEC REC JUMPER # 5 IN ?- YES           SEREC: 377  
IS OPT CLR ENABLE JUMPER # 4 IN ?- YES OPTCLR: 377  
DO YOU HAVE THE EXTERNAL MODEM BYPASS JUMPER  
CONNECTOR ON (H315)- YES                   JMRBY: 377

9. PROGRAM DESCRIPTION
  
10. FLOW CHARTS: RECEIVER FLOW, TRANSMITTER FLOW, TRANSMITTER & RECEIVER FLOW
11. LISTINGS

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.ENABLE ABS

;DU11 DZDUF-C TAPE F  
;COPYRIGHT 1973, DIGITAL EQUIPMENT CORP., MAYNARD, MASS. 01754;STARTING PROCEDURE  
;LOAD PROGRAM  
;PRESS START  
;PROGRAM WILL TYPE "DU11 DZDUF-C TAPE F "  
;PROGRAM WILL TYPE "R" TO INDICATE THAT TESTING HAS STARTED  
;AT THE END OF A PASS, PROGRAM WILL TYPE "END OF PASS TAPE F"  
;AND THEN RESUME TESTING

;SWITCH REGISTER OPTIONS

100000  
040000  
020000  
010000  
004000  
002000  
001000  
000400  
000100  
000040  
000020  
000010  
000004  
000002  
000001SW15=100000  
SW14=40000  
SW13=20000  
SW12=10000  
SW11=4000  
SW10=2000  
SW09=1000  
SW08=400  
SW06=100  
SW05=40  
SW04=20  
SW03=10  
SW02=4  
SW01=2  
SW00=1:=1, HALT ON ERROR  
:=1, LOOP ON CURRENT TEST  
:=1, INHIBIT ERROR TYPEOUT  
  
:=1, INHIBIT ITERATIONS  
:=1, ESCAPE TO NEXT TEST ON ERROR  
:=1, LOOP WITH CURRENT DATA  
:=1, LOOP ON ERROR;LOCK ON TEST SELECT  
;RESTART PROGRAM AT SELECTED TEST  
;RESELECT VECTOR AND CONTROL REGISTER  
;ADDRESS AFTER PROGRAM RESTART

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177570  
177570  
177776  
001100  
  
005746  
005726  
010046  
012600  
024646  
022626  
  
100000  
040000  
020000  
010000  
004000  
002000  
001000  
000400  
000200  
000100  
000040  
000020  
000010  
000004  
000002  
000001  
  
000340  
000300  
000240  
000200  
000140  
000100  
000040  
000000

;REGISTER DEFINITIONS

R0=%0 ;GENERAL REGISTER  
R1=%1 ;GENERAL REGISTER  
R2=%2 ;GENERAL REGISTER  
R3=%3 ;GENERAL REGISTER  
R4=%4 ;GENERAL REGISTER  
R5=%5 ;GENERAL REGISTER  
SP=%6 ;PROCESSOR STACK POINTER  
PC=%7 ;PROGRAM COUNTER

;LOCATION EQUIVALENCIES

DSWR=177570 ;HARDWARE SWITCH REGISTER LOC.  
DLIGHTS=177570 ;HARDWARE DISPLAY REGISTER LOC.  
PS=177776 ;PROCESSOR STATUS WORD  
STACK=1100 ;START OF PROCESSOR STACK

;INSTRUCTION DEFINITIONS

PUSH1SP=5746 ;DECREMENT PROCESSOR STACK 1 WORD =TST -(SP)  
POP1SP=5726 ;INCREMENT PROCESSOR STACK 1 WORD =TST (SP)+  
PUSHRD=10046 ;SAVE R0 ON STACK =MOV R0,-(SP)  
POP RD=12600 ;RESTORE R0 FROM STACK =MOV (SP)+,R0  
PUSH2SP=24646 ;DECREMENT STACK TWICE =CMP -(SP),-(SP)  
POP2SP=22626 ;INCREMENT STACK TWICE =CMP (SP)+,(SP)+  
.EQUIV ENT,HLT ;BASIC DEFINITION OF ERROR CALL

BIT15=100000  
BIT14=40000  
BIT13=20000  
BIT12=10000  
BIT11=4000  
BIT10=2000  
BIT9=1000  
BIT8=400  
BIT7=200  
BIT6=100  
BIT5=40  
BIT4=20  
BIT3=10  
BIT2=4  
BIT1=2  
BIT0=1

;PROCESSOR LEVELS

LEVEL7=340  
LEVEL6=300  
LEVEL5=240  
LEVEL4=200  
LEVEL3=140  
LEVEL2=100  
LEVEL1=040  
LEVEL0=000

```

641 ;REGISTER DEFINITIONS
642
643 100000
644 040000
645 020000
646 010000
647 004000
648 002000
649 001000
650 000400
651 000200
652 000100
653 000040
654 000020
655 000010
656 000004
657 000002
658 000001
659
660 100000
661 040000
662 020000
663 010000
664
665 001000
666 000400
667
668 030000
669 020000
670 000000
671 000000
672 002000
673 004000
674 006000
675 000000
676 001000
677 001400
678
679 100000
680 040000
681 020000
682 002000
683 000400
684 000200
685 000100
686 000040
687 000020
688 000010
689 000001
690
691 000000
692 004000
693 010000
694 014000
695

```

```

;RXCSR BIT DEFINITIONS
DSC=BIT15 :DATA SET CHANGE
RING=BIT14 :RING
CTS=BIT13 :CLR TO SEND
CARDET=BIT12 :CARRIER DETECT
RECACT=BIT11 :REC ACTIVE
SRD=BIT10 :SEC REC DATA
DSR=BIT9 :DATA SET RDY
STPSYN=BIT8 :STRIP SYNC
RXDONE=BIT7 :REC DONE
RINTEN=BIT6 :REC INTR ENABLE
DSINTE=BIT5 :DSC INTR ENABLE
SYNSCH=BIT4 :SYNC SEARCH
STD=BIT3 :SEC XMIT DATA
RTS=BIT2 :REQ TO SEND
DTR=BIT1 :DATA TERM RDY
VOID=BIT0

;RXDBUF BIT DEFINITIONS
RXERR=BIT15 :REC ERROR
OVRUN=BIT14 :OVERRUN
FRMERR=BIT13 :FRAME ERROR
PARER=BIT12 :PARITY ERROR

;PARCSR BIT DEFINITIONS
PAREN=BIT9 :PARITY ENABLE
EVPAR=BIT8 :EVEN PARITY SENSE

;PARCSR WRD DEFINITIONS
SYNINT=30000 :SYNC EXTERNAL MODE
SYNEXT=20000 :SYNC INTERNAL MODE
ISYN00=0 :ISOC MODE
FIVE=0 :WORD LENGTH 5 BITS
SIX=2000 :WORD LENGTH 6 BITS
SEVEN=4000 :WORD LENGTH 7 BITS
EIGHT=6000 :WORD LENGTH 8 BITS
NOPAR=0 :NO PARITY
ODDPAR=1000 :ODD PARITY
EVEPAR=1400 :EVEN PARITY

;TXCSR BIT DEFINITIONS
DNA=BIT15 :DATA NOT AVAILABLE
MTDATA=BIT14 :MAINT DATA
CLK=BIT13 :CLK
BITW=BIT10 :BIT WINDOW
MRESET=BIT8 :MASTER RESET
TXDONE=BIT7 :XMIT DONE
TXINTE=BIT6 :XMIT INTR ENABLE
DNAINTE=BIT5 :DNA INTR ENAB
SEND=BIT4 :SEND
HDX/FDX :HDX/FDX
BREAK :BREAK

;TXCSR WRD DEFINITIONS
USER=0 :USER MODE
MINT=4000 :MAINT INT MODE
MEXT=10000 :MAINT EXT MODE
SYSTST=14000 :SYSTEM TEST MODE
;TRAPCATCHER FOR ILLEGAL INTERRUPTS

```

```

        ;STANDARD INTERRUPT VECTORS
696
697
698
699
700 000024 000024      . =24      .PFAIL      :POWER FAIL HANDLER
701 000026 000340      340          :SERVICE AT LEVEL 7
702 000030 007062      .HLT         :ERROR HANDLER
703 000032 000340      340          :SERVICE AT LEVEL 7
704 000034 007030      .TRPSRV     :GENERAL HANDLER DISPATCH SERVICE
705 000036 000340      340          :SERVICE AT LEVEL 7
706
707      ;SOFTWARE SWITCH REGISTER
708
709      . =174
710 000174 000000      DISPREG: .WORD 0      :SOFTWARE DISPLAY REG.
711 000176 000000      SWREG:  .WORD 0      :SOFTWARE SWITCH REGISTER
712 000200 000167 001054  JMP      .START      :GO TO START OF PROGRAM
713
714
715
716      . =1100
717
718      ;INDIRECT POINTERS
719
720 001100 177570      SMR: 177570      :SWITCH REGISTER POINTER
721 001102 177570      LIGHTS:177570    :DISPLAY REGISTER POINTER
722 001104 177560      TKCSR: 177560    :TELETYPE KEYBOARD CONTROL REGISTER
723 001106 177562      TKDBR: 177562    :TELETYPE KEYBOARD DATA BUFFER
724 001110 177564      TPCSR: 177564    :TELEPRINTER CONTROL REGISTER
725 001112 177566      TPDBR: 177566    :TELEPRINTER DATA BUFFER
726
727      ;PROGRAM CONTROL PARAMETERS
728
729 001114 000000      RTRN: 0          :SCOPE ADDRESS FOR LOOP ON TEST
730 001116 000000      NEXT: 0          :ADDRESS OF NEXT TEST TO BE EXECUTED
731 001120 000000      LOCK: 0          :ADDRESS FOR LOCK ON CURRENT DATA
732 001122 000000      ICOUNT: 0        :NUMBER OF ITERATIONS THAT CURRENT TEST WILL BE EXECUTED
733 001124 000000      LPCNT: 0        :NUMBER OF ITERATIONS COMPLETED
734 001126 000000      TSTNO: 0        :NUMBER OF TEST IN PROGRESS
735 001130 000000      PASCNT: 0       :NUMBER OF PASSES COMPLETED
736 001132 000000      ERRCNT: 0       :TOTAL NUMBER OF ERRORS
737 001134 000000      LSTERR: 0       :PC OF LAST ERROR CALL
738
739      ;PROGRAM VARIABLES
740
741 001136 000020      HOLD: 20        :TEMPORARY STORAGE=DELAY TIME FOR CABLES
742 001140 000000      SHIFT: 0        :TEMPORARY STORAGE = # OF SHIFTS PER CHAR
743 001142 000000      COUNT: 0        :TEMPORARY STORAGE = # OF TIMES A CHAR WILL BE SENT
744 001144 000000      TEMP1: 0        :TEMPORARY STORAGE
745 001146 000000      TEMP2: 0        :TEMPORARY STORAGE
746 001150 000000      TEMP3: 0        :TEMPORARY STORAGE
747 001152 000000      TEMP4: 0        :TEMPORARY STORAGE
748 001154 000000      TEMPS: 0        :TEMPORARY STORAGE
749 001156 000000      SAVR0: 0        :R0 STORAGE
750 001160 000000      SAVR1: 0        :R1 STORAGE
751 001162 000000      SAVR2: 0        :R2 STORAGE
    
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D02

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SEQ 0016

753	001164	000000
754	001166	000000
755	001170	000000
756	001172	000000
756	001174	000000

SAVR3:	0
SAVR4:	0
SAVRS:	0
SAVSP:	0
SAVPC:	0

:R3	STORAGE
:R4	STORAGE
:R5	STORAGE
:STACK	POINTER STORAGE
:PROGRAM	COUNTER STORAGE

```

757 ;PROGRAM CONVERSATIONAL PARAMETERS
758 001176 377 SYNCH0: .BYTE 377 ;# OF SYNC CHARS REQ'D FOR SYNC'ZATION
759 001177 377 SEXMIT: .BYTE 377 ;SEC XMIT JUMPER "IN"
760 001200 377 SEREC: .BYTE 377 ;SEC REC JUMPER "IN"
761 001201 377 OPTCLR: .BYTE 377 ;OPTIONAL JUMPER CLR "IN"
762 001202 000 MULTD: .BYTE 0 ;NO MULTIPLE DEVICE FLAG
763 001203 377 JMRBY: .BYTE 377 ;EXTERNAL MODEM BYPASS JUMPER "IN"
764 .EVEN
765
766 ;PROGRAM MULTIPLE DEVICE PARAMETERS
767 001204 000000 BASEADD: 0 ;PROG CONTROLLED 1ST DEVICE ADDR
768 001206 000000 KEEPADD: 0 ;SAVED 1ST DEVICE ADDR
769 001210 000000 LASTADD: 0 ;LAST DEVICE RXCSR ADDR
770 001212 000000 BASEIV: 0 ;PROG CONTROLLED IV
771 001214 000000 KEEPIV: 0 ;SAVED INTR VECTOR
772 001216 000000 ACTREG: 0 ;ACTIVE REGISTER , , MODIFY THIS
773 ;LOCATION TO DISQUALIFY OR QUALIFY
774 ;DEVICES (1= RUN , 0= DON'T RUN)
775 001220 000000 ROTADD: 0 ;ROTATING POINTER FOR ACTREG. POINTS
776 ;TO DEVICE PRESENTLY UNDER TEST WHEN RUNNING MULTIPLE DE
777
778 ;PROGRAM CONTROL FLAGS
779
780 001222 000 INIFLG: .BYTE 0 ;PROGRAM INITIALIZATION FLAG
781 001223 000 STFLG: .BYTE 0 ;TEST START FLAG
782 001224 000 ERRFLG: .BYTE 0 ;ERROR OCCURED FLAG
783 001225 000 LOKFLG: .BYTE 0 ;LOCK ON CURRENT TEST FLAG
784
785 ;DEFINITIONS FOR TRAP SUBROUTINE CALLS
786 ;POINTERS TO SUBROUTINES CAN BE FOUND
787 ;IN THE TABLE IMMEDIATLY FOLLOWING THE DEFINITIONS
788
789 001226 .TRPTAB:
790 ;*****
791 ;*****
792 104400 .SCOPE SCOPE=TRAP+0 ;CALL TO SCOPE LOOP AND ITERATION HANDLER
793 001226 005614 .SCOP1 SCOP1=TRAP+1 ;CALL TO LOOP ON CURRENT DATA HANDLER
794 104401 .TYPE TYPE=TRAP+2 ;CALL TO TELETYPE OUTPUT ROUTINE
795 001230 006000 .INSTR INSTR=TRAP+3 ;CALL TO ASCII STRING INPUT ROUTINE
796 104402 .INSTER INSTER=TRAP+4 ;CALL TO INPUT ERROR HANDLER
797 001232 006020 .PARAM PARAM=TRAP+5 ;CALL TO NUMERICAL DATA INPUT ROUTINE
798 104403 .SAVOS SAVOS=TRAP+6 ;CALL TO REGISTER SAVE ROUTINE
799 001234 006060 .RESOS RESOS=TRAP+7 ;CALL TO REGISTER RESTORE ROUTINE
800 104404 .CONVRT CONVRT=TRAP+10 ;CALL TO DATA OUTPUT ROUTINE
801 001236 006176 .CNVRT CNVRT=TRAP+11 ;CALL TO DATA OUTPUT ROUTINE WITHOUT CR/LF
802 104405
803 001240 006230 .CNVRT SETFLG=TRAP+12 ;CALL TO FLAG SET ROUTINE
804 104406
805 001242 006444
806 104407
807 001244 006504
808 104410
809 001246 006536
810 104411
811 001250 006542
812 104412

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```

813 001252 006762 .SETFLG
814 104413
815 001254 007476 .CKSMR CKSMR=TRAP+13 ;CALL TO ALLOW SMREG TO BE LOADED FROM TTY
816 104414
817 001256 007552 .CNTLU CNTLU=TRAP+14 ;CALL TO ALLOW LOADING OF SMREG FROM TTY
818 *****
819 *****
820
821 :PROGRAM INITIALIZATION
822 :LOCK OUT INTERRUPTS
823 :SET UP PROCESSOR STACK
824 :SET UP POWER FAIL VECTOR
825 :CLEAR PROGRAM CONTROL FLAGS AND COUNTS
826 :TYPE TITLE MESSAGE
827
828 001260 012767 000340 176510 .START: MOV 8340,PS ;LOCK OUT INTERRUPTS
829 001266 012706 001100 MOV 8STACK,SP ;SET UP STACK
830 001272 012737 007332 000024 MOV 8.PFAIL,2824 ;SET UP POWER FAIL VECTOR
831 001300 005067 177620 CLR LPCNT ;CLEAR # OF ITERATION COMPLETED LOCATION
832 001304 105067 177713 CLRB STFLG ;CLEAR START FLAG
833 001310 005067 177614 CLR PASCNT ;CLEAR PASS COUNT
834 001314 105067 177704 CLRB ERRFLG ;CLEAR ERROR FLAG
835 001320 005067 177606 CLR ERRCNT ;CLEAR ERROR COUNT
836 001324 005067 177604 CLR LSTERR ;CLEAR LAST ERROR POINTER
837 001330 012767 000001 177570 MOV 81,TSTNO ;SET UP FOR TEST 1
838 001336 012767 001260 177550 MOV 8.START,RTRN ;SET UP FOR POWER FAIL BEFORE
839 ;TESTING STARTS
840 001344 105767 177652 TSTB INIFLG ;HAS INITIALIZATION BEEN PERFORMED
841 001350 001004 BNE ONCE
842 001352 104402 007652 TYPE ,MTITLE ;TYPE TITLE MESSAGE
843 001356 105167 177640 COMB INIFLG ;IF NOT SET FLAG AND DO
844 001362 012767 177570 177510 ONCE: MOV 8DSMR,SMR ;RELOAD HARDWARE SWITCH REGISTER INTO POINTER
845 001370 012767 177570 177504 MOV 8DLIGHTS,LIGHTS ;RELOAD HARDWARE DISPLAY REGISTER INTO POINTER
846 001376 013746 000006 MOV 286,-(SP) ;SAVE VECTORS
847 001402 013746 000004 MOV 284,-(SP)
848 001406 012737 001426 000004 MOV 8648,284 ;SET UP FOR TIMEOUT
849 001414 022777 177777 177456 CMP 8-1,2SMR ;REFERENCE HARDWARE SWITCH REGISTER
850 001422 001402 BEQ 658
851 001424 000407 BR 668
852 001426 022626 648: CMP (SP)+,(SP)+ ;ADJUST STACK
853 001430 012767 000176 177442 658: MOV 8SMREG,SMR ;POINT TO SOFTWARE SWITCH REG
854 001436 012767 000174 177436 MOV 8DISPREG,LIGHTS ;POINT TO SOFT DISPLAY REG
855 001444 012637 000004 668: MOV (SP)+,284 ;RESTORE VECTORS
856 001450 012637 000006 MOV (SP)+,286
857 001454 005737 000042 TST 2842 ;UNDER MONITOR
858 001460 001005 BNE 678 ;IS SMREG USED
859 001462 022767 000176 177410 CMP 8SMREG,SMR
860 001470 001001 BNE 678
861 001472 104414 CNTLU
862 001474 032777 000001 177376 678: BIT 8SM00,2SMR ;RESELECT VECTOR & CONTROL REG?
863 001502 001002 BNE 18
864 001504 000167 000446 JMP .BEGIN
865 001510 012700 000300 18: MOV 8300,R0 ;RESTORE VECTOR AREA TO TRAPCATCHER
866 001514 012701 000302 MOV 8302,R1 ;START AT LOCATION 3C0
867 001520 012702 000004 MOV 84,R2
868 001524 010110 28: MOV R1,(R0)

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869	001526	005011			CLR	(R1)	
870	001530	060200			ADD	R2,R0	
871	001532	060201			ADD	R2,R1	
872	001534	022701	001000		CMP	#1000,R1	;END AT LOCATION 776
873	001540	002771			BLT	ZS	
874	001542	104403			INSTR		;OUTPUT MESSAGE & GET INPUT STRING
875	001544	007726			MREGAD		;MESSAGE
876	001546	104405			PARAM		;CONVERT STRING
877	001550	160000			160000		;LOW LIMIT
878	001552	167776			167776		;HIGH LIMIT
879	001554	011562			DUBASE		;STORE AT THIS LOCATION
880	001556	001			1		;MASK
881	001557	001			1		;HOW MANY TIMES + 2
882	001560	016767	007776	177420	MOV	DUBASE,KEEPADD	;SAVE
883	001566	004767	007636		JSR	PC,DUADDR	
884	001572	016767	177410	177404	MOV	KEEPADD,BASEADD	;RESTORE FOR ROTATION
885	001600	104403			INSTR		;OUTPUT MESSAGE & GET INPUT STRING
886	001602	007704			MVECTO		;MESSAGE
887	001604	104405			PARAM		;CONVERT STRING
888	001606	000300			300		;LOW LIMIT
889	001610	000776			776		;HIGH LIMIT
890	001612	012104			DURIV		;STORE AT THIS LOCATION
891	001614	001			1		;MASK
892	001615	004			4		;HOW MANY TIMES + 2
893	001616	016767	010262	177370	MOV	DURIV,KEEPIV	;SAVE
894	001624	016767	010254	177360	MOV	DURIV,BASEIV	;SET UP FOR ROTATION
895	001632	104403			INSTR		;OUTPUT MESSAGE & GET INPUT STRING
896	001634	010007			MULT		;MESSAGE
897	001636	104412			SETFLG		;SET FLAG BASED UPON INPUT STRING
898	001640	001202			MULTD		;THIS FLAG
899	001642	105767	177334		TSTB	MULTD	;ARE THERE MULTIPLE DEVICES
900							;ON THE SYSTEM ?
901	001646	100406			BMI	BBB	;YES,ASK NEXT QUESTION
902	001650	005067	177342		CLR	ACTREG	
903	001654	005067	177340		CLR	ROTADD	
904	001660	000167	000140		JMP	OUTMUL	;JUMP AROUND NEXT QUESTION
905	001664				BBB:		
906	001664	104403			INSTR		;OUTPUT MESSAGE & GET INPUT STRING
907	001666	010066			MLASTD		;MESSAGE
908	001670	104405			PARAM		;CONVERT STRING
909	001672	160000			160000		;LOW LIMIT
910	001674	167776			167776		;HIGH LIMIT
911	001676	001210			LASTADD		;STORE AT THIS LOCATION
912	001700	001			1		;MASK
913	001701	001			1		;HOW MANY TIMES + 2
914							;THE FOLLOWING ROUTINE SETS UP ACTREG FOR THE FIRST TIME
915	001702	012767	000001	177310	1S: MOV	#1,ROTADD	;SET UP POINTER
916	001710	005067	177302		CLR	ACTREG	;CLR ACTIVE REGISTER
917	001714	056767	177300	177274	2S: BIS	ROTADD,ACTREG	;MAKE THIS DEVICE ACTIVE
918	001722	000241			CLC		
919	001724	006167	177270		ROL	ROTADD	;SET UP POINTER
920	001730	103421			BCS	3S	;ARE YOU OUT OF RANGE ?
921	001732	062767	000010	177244	ADD	#10,BASEADD	;SET UP BASE ADDRESS
922	001740	026767	177244	177236	CMP	LASTADD,BASEADD	;IS THIS THE LAST DEVICE ?
923	001746	101362			BMI	ZS	;NO DO IT AGAIN
924	001750	056767	177244	177240	BIS	ROTADD,ACTREG	;THIS ASSUMES THAT THERE ARE AT

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925 ;LEAST TWO DEVICES WHEN YOU ANSWER YES TO
926 ;MULTIPLE DEVICE QUESTION
927 001756 012767 000001 177234 4S:  MOV      $1,ROTADD ;SET UP FOR LATER USE IN END OF PASS ROUTINE
928 001764 016767 177216 177212  MOV      KEEPADD,BASEADD ;DITTO
929 001772 000414  BR      OUTMUL ;CONTINUE QUESTIONS
930 001774 016767 177206 177202 3S:  MOV      KEEPADD,BASEADD ;RESTORE
931 002002 104403  INSTR   ;OUTPUT MESSAGE & GET INPUT STRING
932 002004 010251  MRRANGE ;MESSAGE
933 002006 104405  PARAM   ;CONVERT STRING
934 002010 160000  160000  ;LOW LIMIT
935 002012 167776  167776  ;HIGH LIMIT
936 002014 001210  LASTADD ;STORE AT THIS LOCATION
937 002016 001      .BYTE 1 ;MASK
938 002017 001      .BYTE 1 ;HOW MANY TIMES + 2
939 002020 000167 177656  JMP      IS      ;DO IT AGAIN
940 002024  OUTMUL:
941 002024 104403  INSTR   ;OUTPUT MESSAGE & GET INPUT STRING
942 002026 010535  MLEVEL  ;MESSAGE
943 002030 104405  PARAM   ;CONVERT STRING
944 002032 000004  4       ;LOW LIMIT
945 002034 000007  7       ;HIGH LIMIT
946 002036 011424  DUPRT   ;STORE AT THIS LOCATION
947 002040 000      .BYTE 0 ;MASK
948 002041 001      .BYTE 1 ;HOW MANY TIMES + 2
949 002042 004767 007306  JSR      PC,DULEV
950 ;COMPARE THE FIRST CHARACTER IN THE TELETYPE INPUT
951 ;BUFFER TO THE CHARACTERS "1" AND "2"
952 ;IF THE CHARACTER IS "1" CLEAR THE FLAG
953 ;IF THE CHARACTER IS "2" SET THE FLAG
954 002046  AAA:
955 002046 104403  INSTR   ;OUTPUT MESSAGE & GET INPUT STRING
956 002050 010562  MSYNC   ;MESSAGE
957 002052 122767 000061 007134 3S:  CHPB    #'1,INBUF ;IS IT "1" ?
958 002060 001003  BNE     IS      ;000
959 002062 105067 177110  CLAB    SYNCNO
960 002066 000412  BR      4S
961 002070 122767 000062 007116 1S:  CHPB    #'2,INBUF ;IS IT "2" ?
962 002076 001004  BNE     2S
963 002100 112767 177777 177070  MOVB    #-1,SYNCNO ;377
964 002106 000402  BR      4S
965 002110 104404  2S:  INSTR   ;RETRY
966 002112 000757  BR      3S
967 002114 000240  4S:  NOP
968 002116 104403  INSTR   ;OUTPUT MESSAGE & GET INPUT STRING
969 002120 010630  MIRE6   ;MESSAGE
970 002122 104412  SETFLG  ;SET FLAG BASED UPON INPUT STRING
971 002124 001177  SEXMIT  ;THIS FLAG
972 002126 104403  INSTR   ;OUTPUT MESSAGE & GET INPUT STRING
973 002130 010676  MIRE5   ;MESSAGE
974 002132 104412  SETFLG  ;SET FLAG BASED UPON INPUT STRING
975 002134 001200  SEREC   ;THIS FLAG
976 002136 104403  INSTR   ;OUTPUT MESSAGE & GET INPUT STRING
977 002140 010743  MIRE4   ;MESSAGE
978 002142 104412  SETFLG  ;SET FLAG BASED UPON INPUT STRING
979 002144 001201  OPTCLR  ;THIS FLAG
980 002146 104403  INSTR   ;OUTPUT MESSAGE & GET INPUT STRING

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981 002150 011017 NEXTJ ;MESSAGE
982 002152 104412 SETFLG ;SET FLAG BASED UPON INPUT STRING
983 002154 001203 JMRBY ;THIS FLAG
984
985 ;TEST START AND RESTART
986
987 002156 012767 000340 175612 .BEGIN: MOV #340,PS ;LOCK OUT INTERRUPTS
988 002164 012706 001100 MOV #STACK,SP ;SET UP STACK
989 002170 005737 000042 TST #42 ;IS PROGRAM UNDER MONITOR CONTROL
990 002174 001056 BNE 35
991 002176 105767 177000 TSTB MULTD ;DON'T ALLOW LOCK ON TEST IF RUNNING
992 ;MULTIPLE DEVICES
993
994 002202 001407 BEQ 55 ;IF NO TEST FOR LOCK ON TEST
995 002204 016767 003564 003464 MOV BRW,TTST ;RESTORE NORMAL SCOPE LOOP
996 002212 016767 003560 003460 MOV BRX,TTST+2 ;DITTO
997 002220 000444 BR 35 ;JUMP AROUND IF YES
998 002222 032777 000004 176650 55: BIT #BIT2,SMR ;CHECK FOR LOCK ON TEST
999 002230 001416 BEQ 15
1000 002232 104403 INSTR ;OUTPUT MESSAGE & GET INPUT STRING
1001 002234 010472 MLOCK ;MESSAGE
1002 002236 104412 SETFLG ;SET FLAG BASED UPON INPUT STRING
1003 002240 001225 LOKFLG ;THIS FLAG
1004 002242 105767 176757 TSTB LOKFLG ;IS LOCK ON TEST OPTION SELECTED
1005 002246 001407 BEQ 15
1006 002250 012767 000240 003420 MOV #NOP,TTST
1007 002256 012767 000240 003414 MOV #NOP,TTST+2 ;SET UP TO LOCK
1008 002264 000406 BR 25
1009 002266 016767 003502 003402 15: MOV BRW,TTST
1010 002274 016767 003476 003376 MOV BRX,TTST+2 ;LOCK NOT SELECTED, SET UP FOR NORMAL SCOPE LOOP
1011 002302 032777 000002 176570 25: BIT #SM01,SMR ;IF SM01=1, GET STARTING PC
1012 002310 001410 BEQ 35
1013 002312 104403 INSTR ;OUTPUT MESSAGE & GET INPUT STRING
1014 002314 010457 MTSTPC ;MESSAGE
1015 002316 104405 PARAM ;CONVERT STRING
1016 002320 002350 TST1 ;LOW LIMIT
1017 002322 005004 TLAST ;HIGH LIMIT
1018 002324 001114 RTRN ;STORE AT THIS LOCATION
1019 002326 001 .BYTE 1 ;MASK
1020 002327 001 .BYTE 1 ;HOW MANY TIMES + 2
1021 002330 000403 BR 45
1022 002332 012767 002350 176554 35: MOV #TST1,RTRN ;START AT TEST 1
1023 002340 104402 010453 45: TYPE #R ;TYPE R
1024 002344 000177 176544 JMP #RTRN ;START TESTING
    
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1025      ; THIS TEST VERIFYS MATCH DETECT & DATA RDY
1026      ; FLAGS FOR EVERY POSSIBLE MATCH CHARACTER
1027      ; BY OBSERVING RECACT BIT
1028      ; IT WILL TAKE TWO SYNC # CHARACTERS TO GET RECACT BIT
1029      ; DEPENDENT ON MONITOR .....
1030      ; IF ONE SYNC STRAP IS SELECTED IT WILL
1031      ; ONLY TAKE ONE SYNC CHARACTER BEFORE RECACT TO
1032      ; ASSERT
1033      ; MODE: SYNC INTERNAL
1034      ; LENGTH: FIVE
1035      ; SYNC CHARACTER FOR MATCH: B/C
1036      ; THIS TEST USES THE TRANSMITTER AND RECEIVER CHIPS
1037
1038 002350 012767 000001 176550 TST1:  MOV    #1,TSTNO      ;SAVE THIS
1039 002356 012767 002676 176532      MOV    #TST2,NEXT    ;GO TO THIS TEST WHEN THRU
1040 002364 012767 002500 176526      MOV    #3$,LOCK     ;SET UP FOR SCOPE LOOP
1041 002372 052777 000400 007474      BIS    #MRESET,@TXCSR ;MASTER RESET
1042 002400 016703 007460      MOV    #RXDBUF,R3    ;SET UP FOR ERROR MESSAGE
1043      ;SET SYNC INTERNAL, FIVE NO PARITY 0 SYNC REGISTER
1044 002404 012704 030000      MOV    #SYNINT!FIVE!NOPAR,R4 ;CREATE PARAMETERS
1045 002410 012777 004020 007456 1$:    MOV    #MINT!SEND,@TXCSR ;SET SEND & MAINT INTER
1046 002416 010477 007446      MOV    R4,@PARCSR    ;LOAD CSR
1047 002422 052777 000020 007430      BIS    #SYNSCH,@RXCSR ;SET SYNC SEARCH
1048      ;POKE CLK TO GET INTO SYNCHRONIZATION
1049      ;BOTH THE LOGIC & RECEIVER
1050 002430 052777 020000 007436      BIS    #CLK,@TXCSR   ;POKE CLK UP
1051 002436 042777 020000 007430      BIC    #CLK,@TXCSR   ;POKE CLK DOWN
1052 002444 110477 007430      MOVB  R4,@TXDBUF    ;LOAD DATA CHARACTER
1053      ;POKE CLK TO GET TRANSMITTER & RECEIVER INTO SYNCHRONIZATION
1054 002450 052777 020000 007416      BIS    #CLK,@TXCSR   ;POKE CLK UP
1055 002456 042777 020000 007410      BIC    #CLK,@TXCSR   ;POKE CLK DOWN
1056 002464 032777 004000 007366      BIT    #RECACT,@RXCSR ;RECACT ?
1057 002472 001401      BEQ   2$
1058 002474 104000      HLT
1059 002476      2$:
1060 002476 000404      BR    4$
1061 002500 010477 007364      3$:  MOV    R4,@PARCSR    ;LOAD PARCSR WITH PARAMETERS
1062 002504 110477 007370      MOVB  R4,@TXDBUF    ;LOAD SYNC CHAR
1063 002510 012767 000002 176424 4$:  MOV    #2,COUNT     ;# OF SYNC CHARS
1064 002516 005777 007352      5$:  TST   @TXCSR        ;DNA ?
1065 002522 100001      BPL   6$
1066 002524 104000      HLT
1067 002526      6$:
1068      ;IT SHOULD BE CLEARED FROM PREVIOUS READ
1069 002526 012767 000005 176404      7$:  MOV    #5,SHIFT     ;# OF SHIFTS
1070 002534
1071 002534 052777 020000 007332      BIS    #CLK,@TXCSR   ;POKE CLK UP
1072 002542 042777 020000 007324      BIC    #CLK,@TXCSR   ;POKE CLK DOWN
1073 002550 005367 176364      DEC   SHIFT        ;# OF SHIFTS
1074 002554 001367      BNE   7$
1075 002556 005367 176360      DEC   COUNT        ;# OF SYNC CHARS
1076 002562 001403      BEQ   8$
1077      ;TEST SYNCNO TO SEE HOW MANY SYNC CHARACTERS NEEDED
1078 002564 105767 176406      TSTB  SYNCNO
1079 002570 100752      BMI   5$           ;TWO SYNC CHARACTERS..
1080 002572 032777 004000 007260 8$:  BIT    #RECACT,@RXCSR ;RECACT ?

```

# K02

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SEQ 0023

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1081 002600 001001          BNE      95
1082 002602 104000          HLT
1083 002604                95:
1084                          ;REACT FAILED TO SET, POSSIBLE
1085                          ; THAT THE RECEIVER FAILED TO MATCH
1086                          ; THE SYNC CHARACTER
1086 002604 017701 007254    MOV      @RXDBUF,R1      ;SAVE ACTUAL
1087 002610 010400          MOV      R4,R0          ;SAVE EXPECTED
1088 002612 042700 177400    BIC      @177400,R0     ;CLR UPPER BYTE
1089 002616 020001          CMP      R0,R1         ;DO THEY COMPARE ?
1090 002620 001401          BEQ      105
1091 002622 104002          HLT
1092 002624                105:
1093                          ;IF REACT FAILED ALONG WITH THIS
1094                          ;... IT PROBABLY IS A TRANSMITTER ERROR
1095                          ; HOWEVER... IF ONLY THIS FAILED IT
1096                          ; PROBABLY IS A RECEIVER ERROR
1096 002624 104401          SCOPE1
1097                          ;POKE CLK TO SEE DNA... DNA COMES UP ON THE FIRST
1098                          ;BIT OF THE NEXT CHARACTER IF NO CHARACTER IS LOADED INTO
1099                          ;TXDBUF
1100 002626 052777 020000 007240  BIS      @CLK,@TXCSR   ;POKE CLK UP
1101 002634 005777 007234    TST      @TXCSR       ;DNA?
1102 002640 100401          BMI      115
1103 002642 104000          HLT
1104 002644                115:
1105                          ;SET UP CONDITIONS FOR NEXT SYNC CHARACTER MATCH
1106 002644 052777 000400 007222  BIS      @MRESET,@TXCSR ;MASTER RESET
1107 002652 032777 000020 007200  BIT      @SYNSCH,@TXCSR ;SYNC SEARCH = 0 ?
1108 002660 001401          BEQ      125
1109 002662 104000          HLT
1110 002664                125:
1111 002664 005204          INC      R4
1112 002666 122704 000040    CMPB    @40,R4        ;IS THIS THE LAST CHARACTER ?
1113 002672 001246          BNE      15
1114 002674 104400          SCOPE
  
```



```

1115                                     ;; THIS TEST VERIFYS MATCH DETECT & DATA RDY
1116                                     ;; FLAGS FOR EVERY POSSIBLE MATCH CHARACTER
1117                                     ;; BY OBSERVING RECACT BIT
1118                                     ;; IT WILL TAKE TWO SYNC # CHARACTERS TO GET RECACT BIT
1119                                     ;; * DEPENDENT ON MONITOR
1120                                     ;; IF ONE SYNC STRAP IS SELECTED IT WILL
1121                                     ;; ONLY TAKE ONE SYNC CHARACTER BEFORE RECACT TO
1122                                     ;; ASSERT
1123                                     ;; MODE: SYNC INTERNAL
1124                                     ;; LENGTH: SIX
1125                                     ;; SYNC CHARACTER FOR MATCH: B/C
1126                                     ;; THIS TEST USES THE TRANSMITTER AND RECEIVER CHIPS
1127
1128 002676 012767 000002 176222 TST2:  MOV     #2,TSTNO           ;SAVE THIS
1129 002704 012767 003224 176204      MOV     #TST3,NEXT         ;GO TO THIS TEST WHEN THRU
1130 002712 012767 003026 176200      MOV     #3$,LOCK          ;SET UP FOR SCOPE LOOP
1131 002720 052777 000400 007146      BIS     #MRESET,@TXCSR    ;MASTER RESET
1132 002726 016703 007132              MOV     RXDBUF,R3         ;SET UP FOR ERROR MESSAGE
1133                                     ;SET SYNC INTERNAL,SIX,NO PARITY,0 SYNC REGISTER
1134 002732 012704 032000              MOV     #SYNINT!SIX!NOPAR,R4 ;CREATE PARAMETERS
1135 002736 012777 004020 007130 1S:   MOV     #MINT!SEND,@TXCSR    ;SET SEND & MAINT INTER
1136 002744 010477 007120              MOV     R4,@PARCSR        ;LOAD CSR
1137 002750 052777 000020 007102      BIS     #SYNSCH,@RXCSR    ;SET SYNC SEARCH
1138                                     ;POKE CLK TO GET INTO SYNCHRONIZATION
1139                                     ;BOTH THE LOGIC & RECEIVER
1140 002756 052777 020000 007110      BIS     #CLK,@TXCSR       ;POKE CLK UP
1141 002764 042777 020000 007102      BIC     #CLK,@TXCSR       ;POKE CLK DOWN
1142 002772 110477 007102              MOV     R4,@TXDBUF        ;LOAD DATA CHARACTER
1143                                     ;POKE CLK TO GET TRANSMITTER & RECEIVER INTO SYNCHRONIZATION
1144 002776 052777 020000 007070      BIS     #CLK,@TXCSR       ;POKE CLK UP
1145 003004 042777 020000 007062      BIC     #CLK,@TXCSR       ;POKE CLK DOWN
1146 003012 032777 004000 007040      BIT     #RECACT,@RXCSR    ;RECACT ?
1147 003020 001401              BEQ     2$
1148 003022 104000              HLT
1149 003024              2$:
1150 003024 000404              BR      4$
1151 003026 010477 007036 3$:   MOV     R4,@PARCSR        ;LOAD PARCSR WITH PARAMETERS
1152 003032 110477 007042              MOV     R4,@TXDBUF        ;LOAD SYNC CHAR
1153 003036 012767 000002 176076 4$:   MOV     #2,COUNT          ;# OF SYNC CHARS
1154 003044 005777 007024 5$:   TST     @TXCSR            ;DNA ?
1155 003050 100001              BPL     6$                ;BR IF NOT SET
1156 003052 104000              HLT
1157 003054              6$:
1158                                     ;IT SHOULD BE CLEARED FROM PREVIOUS READ
1159 003054 012767 000006 176056      MOV     #6,SHIFT          ;# OF SHIFTS
1160 003062              7$:
1161 003062 052777 020000 007004      BIS     #CLK,@TXCSR       ;POKE CLK UP
1162 003070 042777 020000 006776      BIC     #CLK,@TXCSR       ;POKE CLK DOWN
1163 003076 005367 176036              DEC     SHIFT            ;# OF SHIFTS
1164 003102 001367              SNE     7$
1165 003104 005367 176032              DEC     COUNT            ;# OF SYNC CHARS
1166 003110 001403              BEQ     8$
1167                                     ;TEST SYNCNO TO SEE HOW MANY SYNC CHARACTERS NEEDED
1168 003112 105767 176060              TSTB   SYNCNO
1169 003116 100752              BHI     5$                ;TWO SYNC CHARACTERS..
1170 003120 032777 004000 006732 8$:   BIT     #RECACT,@RXCSR    ;RECACT ?
    
```



```

1205      ; THIS TEST VERIFYS MATCH DETECT & DATA RDY
1206      ; FLAGS FOR EVERY POSSIBLE MATCH CHARACTER
1207      ; BY OBSERVING RECACT BIT
1208      ; IT WILL TAKE TWO SYNC * CHARACTERS TO GET RECACT BIT
1209      ; * DEPENDENT ON MONITOR
1210      ; IF ONE SYNC STRAP IS SELECTED, IT WILL
1211      ; ONLY TAKE ONE SYNC CHARACTER BEFORE RECACT TO
1212      ; ASSERT
1213      ; MODE: SYNC INTERNAL
1214      ; LENGTH: SEVEN
1215      ; SYNC CHARACTER FOR MATCH: B/C
1216      ; THIS TEST USES THE TRANSMITTER AND RECEIVER CHIPS
1217
1218 003224 012767 000003 175674 TST3:  MOV    #3,TSTNO      ;SAVE THIS
1219 003232 012767 003552 175656      MOV    #TST4,NEXT    ;GO TO THIS TEST WHEN THRU
1220 003240 012767 003354 175652      MOV    #3$,LOCK      ;SET UP FOR SCOPE LOOP
1221 003246 052777 000400 006620      BIS    #MRESET,@TXCSR ;MASTER RESET
1222 003254 016703 006604      MOV    RXDBUF,R3     ;SET UP FOR ERROR MESSAGE
1223      ;SET SYNC INTERNAL, SEVEN, NO PARITY, 0 SYNC REGISTER
1224 003260 012704 034000      MOV    #SYNINT!SEVEN!NOPAR,R4 ;CREATE PARAMETERS
1225 003264 012777 004020 006602 15:    MOV    #MINT!SEND,@TXCSR ;SET SEND & MAINT INTER
1226 003272 010477 006572      MOV    R4,@PARCSR    ;LOAD CSR
1227 003276 052777 000020 006554      BIS    #SYNSCH,@RXCSR ;SET SYNC SEARCH
1228      ;POKE CLK TO GET INTO SYNCHRONIZATION
1229      ;BOTH THE LOGIC & RECEIVER
1230 003304 052777 020000 006562      BIS    #CLK,@TXCSR    ;POKE CLK UP
1231 003312 042777 020000 006554      BIC    #CLK,@TXCSR    ;POKE CLK DOWN
1232 003320 110477 006554      MOVB  R4,@TXDBUF     ;LOAD DATA CHARACTER
1233      ;POKE CLK TO GET TRANSMITTER & RECEIVER INTO SYNCHRONIZATION
1234 003324 052777 020000 006542      BIS    #CLK,@TXCSR    ;POKE CLK UP
1235 003332 042777 020000 006534      BIC    #CLK,@TXCSR    ;POKE CLK DOWN
1236 003340 032777 004000 006512      BIT    #RECACT,@RXCSR ;RECACT ?
1237 003346 001401      BEQ    2$
1238 003350 104000      HLT
1239      ;RECACT SHOULD NOT BE SET
1240      2$:
1241 003352 000404      BR     4$
1242 003354 010477 006510 3$:    MOV    R4,@PARCSR    ;LOAD PARCSR WITH PARAMETERS
1243 003360 110477 006514      MOVB  R4,@TXDBUF     ;LOAD SYNC CHAR
1244 003364 012767 000002 175550 4$:    MOV    #2,COUNT      ;# OF SYNC CHARS
1245 003372 005777 006476 5$:    TST    @TXCSR        ;DNA ?
1246 003376 100001      BPL    6$            ;BR IF NOT SET
1247 003400 104000      HLT
1248      ;DNA SHOULD NOT BE SET OR....
1249      6$:
1250      ;IT SHOULD BE CLEARED FROM PREVIOUS READ
1251 003402 012767 000007 175530 7$:    MOV    #7,SHIFT      ;# OF SHIFTS
1252 003410      BIS    #CLK,@TXCSR    ;POKE CLK UP
1253 003416 042777 020000 006456      BIC    #CLK,@TXCSR    ;POKE CLK DOWN
1254 003424 005367 175510      DEC    SHIFT         ;# OF SHIFTS
1255 003430 001367      BNE    7$
1256 003432 005367 175504      DEC    COUNT         ;# OF SYNC CHARS
1257 003436 001403      BEQ    8$
1258      ;TEST SYNCNO TO SEE HOW MANY SYNC CHARACTERS NEEDED
1259 003440 105767 175532      TSTB  SYNCNO
1260 003444 100752      BMI    5$            ;TWO SYNC CHARACTERS..
1260 003446 032777 004000 006404 8$:    BIT    #RECACT,@RXCSR ;RECACT ?
    
```

```

1261 003454 001001      BNE      95
1262 003456 104000      HLT
1263 003460              95:
1264
1265
1266 003460 017701 006400      MOV      @RXDBUF,R1      ;SAVE ACTUAL
1267 003464 010400      MOV      R4,R0          ;SAVE EXPECTED
1268 003466 042700 177400      BIC      @177400,R0      ;CLR UPPER BYTE
1269 003472 020001      CMP      R0,R1          ;DO THEY COMPARE ?
1270 003474 001401      BEQ      105
1271 003476 104002      HLT
1272 003500              105:
1273
1274
1275
1276 003500 104401      SCOPE1
1277
1278      ;POKE CLK TO SEE DNA...DNA COMES UP ON THE FIRST
1279      ;BIT OF THE NEXT CHARACTER IF NO CHARACTER IS LOADED INTO
1280      ;TXDBUF
1280 003502 052777 020000 006364      BIS      @CLK,@TXCSR      ;POKE CLK UP
1281 003510 005777 006360      TST      @TXCSR      ;DNA?
1282 003514 100401      BMI      115
1283 003516 104000      HLT
1284 003520              115:
1285
1286      ;SET UP CONDITIONS FOR NEXT SYNC CHARACTER MATCH
1286 003520 052777 000400 006346      BIS      @MRESET,@TXCSR ;MASTER RESET
1287 003526 032777 000020 006324      BIT      @SYNSCH,@TXCSR ;SYNC SEARCH = 0 ?
1288 003534 001401      BEQ      125
1289 003536 104000      HLT
1290 003540              125:
1291 003540 005204      INC      R4
1292 003542 122704 000200      CMPB    @200,R4      ;IS THIS THE LAST CHARACTER ?
1293 003546 001246      BNE      15
1294 003550 104400      HLT
1294
    SCOPE
    
```

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1295
1296
1297
1298
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1300
1301
1302
1303
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1305
1306
1307
1308 003552 012767 000004 175346 TST4: MOV R4,TSTNO ;SAVE THIS
1309 003560 012767 004100 175330 MOV R1,TSTS_NEXT ;GO TO THIS TEST WHEN THRU
1310 003566 012767 003702 175324 MOV R3,LOCK ;SET UP FOR SCOPE LOOP
1311 003574 052777 000400 006272 BIS R0,RESET,ATXCSR ;MASTER RESET
1312 003602 016703 006256 MOV R0,RXDBUF,R3 ;SET UP FOR ERROR MESSAGE
1313 ;SET SYNC INTERNAL,EIGHT,NO PARITY,0 SYNC REGISTER
1314 003606 012704 036000 MOV R5,SYNINT,EIGHT,NOPAR,R4 ;CREATE PARAMETERS
1315 003612 012777 004020 006254 15: MOV R0,INT,SEND,ATXCSR ;SET SEND & MAINT INTER
1316 003620 010477 006244 MOV R4,PARCSR ;LOAD CSR
1317 003624 052777 000020 006226 BIS R5,SYNSCH,ATXCSR ;SET SYNC SEARCH
1318 ;POKE CLK TO GET INTO SYNCHRONIZATION
1319 ;BOTH THE LOGIC & RECEIVER
1320 003632 052777 020000 006234 BIS R0,CLK,ATXCSR ;POKE CLK UP
1321 003640 042777 020000 006226 BIC R0,CLK,ATXCSR ;POKE CLK DOWN
1322 003646 110477 006226 MOV R4,ATXDBUF ;LOAD DATA CHARACTER
1323 ;POKE CLK TO GET TRANSMITTER & RECEIVER INTO SYNCHRONIZATION
1324 003652 052777 020000 006214 BIS R0,CLK,ATXCSR ;POKE CLK UP
1325 003660 042777 020000 006206 BIC R0,CLK,ATXCSR ;POKE CLK DOWN
1326 003666 032777 004000 006164 BIT R0,RECACT,ATXCSR ;RECACT ?
1327 003674 001401 BEQ Z5
1328 003676 104000 HLT ;RECACT SHOULD NOT BE SET
1329 003700 Z5:
1330 003700 000404 BR R4
1331 003702 010477 006162 35: MOV R4,PARCSR ;LOAD PARCSR WITH PARAMETERS
1332 003706 110477 006166 MOV R4,ATXDBUF ;LOAD SYNC CHAR
1333 003712 012767 000002 175222 45: MOV R2,COUNT ;# OF SYNC CHARS
1334 003720 005777 006150 55: TST ATXCSR ;DNA ?
1335 003724 100001 BPL Z5 ;BR IF NOT SET
1336 003726 104000 HLT ;DNA SHOULD NOT BE SET OR....
1337 003730 65:
1338 ;IT SHOULD BE CLEARED FROM PREVIOUS READ
1339 003730 012767 000010 175202 75: MOV R0,SHIFT ;# OF SHIFTS
1340 003736 052777 020000 006130 BIS R0,CLK,ATXCSR ;POKE CLK UP
1341 003736 042777 020000 006122 BIC R0,CLK,ATXCSR ;POKE CLK DOWN
1342 003744 005367 175162 DEC SHIFT ;# OF SHIFTS
1343 003752 001367 BNE Z5
1344 003756 005367 175156 DEC COUNT ;# OF SYNC CHARS
1345 003760 001403 BEQ Z5
1346 ;TEST SYNCNO TO SEE HOW MANY SYNC CHARACTERS NEEDED
1347 003766 105767 175204 TSTB SYNCNO
1348 003772 100752 BMI Z5 ;TWO SYNC CHARACTERS..
1349 003774 032777 004000 006056 85: BIT R0,RECACT,ATXCSR ;RECACT ?

```

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1351 004002 001001      BNE      99
1352 004004 104000      HLT
1353 004006      99:
1354
1355
1356 004006 017701 006052      MOV      @RXDBUF,R1      ;SAVE ACTUAL
1357 004012 010400      MOV      R4,R0          ;SAVE EXPECTED
1358 004014 042700 177400      BTR      @177400,R0     ;CLR UPPER BYTE
1359 004020 020001      CMP      R0,R1         ;DO THEY COMPARE ?
1360 004022 001401      BEQ      109
1361 004024 104002      HLT
1362 004026      109:
1363
1364
1365
1366 004026 104401      SCOPE1
1367
1368 :POKE CLK TO SEE DNA...DNA COMES UP ON THE FIRST
1369 :BIT OF THE NEXT CHARACTER IF NO CHARACTER IS LOADED INTO
1370 :TXDBUF
1371 004030 052777 020000 006036      BIS      @CLK,@TXCSR    ;POKE CLK UP
1372 004036 005777 006032      TST      @TXCSR        ;DNA?
1373 004042 100401      BMI      119
1374 004044 104000      HLT
1375
1376 :SET UP CONDITIONS FOR NEXT SYNC CHARACTER MATCH
1377 004046 052777 000400 006020      BIS      @RESET,@TXCSR ;MASTER RESET
1378 004054 032777 000020 005776      BIT      @SYNSCH,@RXCSR ;SYNC SEARCH = 0 ?
1379 004062 001401      BEQ      129
1380 004064 104000      HLT
1381
1382 :SYNC SEARCH SHOULD BE NOT SET
1383 004066 005204      INC      R4
1384 004070 122704 000000      CMPB    @0,R4         ;IS THIS THE LAST CHARACTER ?
1385 004074 001246      BNE
1386 004076 104400      SCOPE

```

# E03

```

1385                                     ; THIS TEST PERFORMS A BINARY COUNT DATA PATTERN ON
1386                                     ; BOTH THE TRANSMITTER AND RECEIVER LOGIC
1387                                     ; MODE: SYNC EXTERNAL (SYNEXT)
1388                                     ; LENGTH: EIGHT PLUS PARITY
1389                                     ; PARITY: EVEPAR
1390                                     ; MAINT. MODE: MINT
1391
1392 004100 012767 000005 175020 TSTS: MOV      #5, TSTNO      ; SAVE THIS
1393 004106 012767 004312 175002      MOV      @TST6, NEXT      ; GO TO THIS TEST WHEN THRU
1394 004114 052777 000400 005752      BIS      @MRESET, @TXCSR  ; MASTER RESET
1395 004122 012777 020000 005740      MOV      @SYNEXT, @PARCSR ; SET THE MODE
1396 004130 052777 000400 005736      BIS      @MRESET, @TXCSR  ; MASTER RESET
1397
1398                                     ; SET MAINTENANCE MODE & SEND
1399                                     ; NOTE: BIT WINDOWS & CLK ARE CLEARED (MTDATA=0)
1400 004136 012777 004020 005730      MOV      @MINT!SEND, @TXCSR
1401
1402                                     ; SET MODE, # OF BITS, PARITY SENSE, & LOAD SYNC REG
1403 004144 012777 027426 005716      MOV      @SYNEXT!EIGHT!EVEPAR!26, @PARCSR
1404 004152 016703 005706      MOV      @RXDBUF, R3      ; SETUP FOR ERROR MSG
1405 004156 005004      CLR      R4              ; FOR DATA CHAR CREATION
1406 004160 110477 005714      MOV      @R4, @TXDBUF     ; LOAD CHARACTER
1407 004164 052777 000020 005666      BIS      @SYNSCH, @RXCSR  ; SET SEARCH SYNC
1408
1409                                     ; GET INTO SYNCHRONIZATION
1410 004172 052777 020000 005674      BIS      @CLK, @TXCSR     ; POKE CLK UP
1411 004200 042777 020000 005666      BIC      @CLK, @TXCSR     ; POKE CLK DOWN
1412 004206 012767 000011 174724 1S:  MOV      @9, SHIFT      ; # OF SHIFTS
1413 004214 010400      MOV      R4, R0          ; EXPECTED
1414 004216 052777 020000 005650 2S:  BIS      @CLK, @TXCSR     ; POKE CLK UP
1415 004224 042777 020000 005642      BIC      @CLK, @TXCSR     ; POKE CLK DOWN
1416 004232 005367 174702      DEC      SHIFT          ; # OF SHIFTS
1417 004236 022767 000003 174674      CMP      @3, SHIFT      ; TIME TO LOAD NEXT CHAR ?
1418 004244 001003      BNE      #3             ; NO ?
1419 004246 005204      INC      R4              ; GENERATE NEXT CHAR
1420 004250 110477 005624      MOV      @R4, @TXDBUF     ; LOAD NEXT CHARACTER
1421 004254 005767 174660 3S:  TST      SHIFT          ; IS IT 0 ?
1422 004260 001356      BNE      #2             ; NO
1423 004262 105777 005572      TSTB    @RXCSR           ; RXDONE = 1 ?
1424 004264 100401      BMI      #5             ; NO
1425 004266 104000      HLT                       ; RXDONE SHOULD BE SET
1426 004270 005566 5S:  MOV      @RXDBUF, R1      ; ACTUAL
1427 004272 017701 005566      CMP      R0, R1          ; COMPARE EXP VS ACT
1428 004276 020001      BEQ      #6             ; NO
1429 004300 001401      BEQ      #6             ; NO
1430 004302 104002      HLT                       ; CHARACTERS SHOULD COMPARE
1431 004304 005566 6S:  TSTB    R4              ; LAST CHARACTER ?
1432 004304 105704      BNE      #18            ; NO
1433 004306 001337      BNE      #18            ; NO
1434 004310 104400 4S:  SCOPE

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# F03

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SEQ 0031

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1435                                     ;; THIS TEST PERFORMS A BINARY COUNT DATA PATTERN ON
1436                                     ;; BOTH THE TRANSMITTER AND RECEIVER LOGIC
1437                                     ;; MODE: SYNC EXTERNAL (SYNEXT)
1438                                     ;; LENGTH: EIGHT PLUS PARITY
1439                                     ;; PARITY: ODDPAR
1440                                     ;; MAINT. MODE: MINT
1441
1442 004312 012767 000006 174606 TST6: MOV      @6, TSTNO          ;SAVE THIS
1443 004320 012767 004524 174570      MOV      @TST7, NEXT          ;GO TO THIS TEST WHEN THRU
1444 004326 052777 000400 005540      BIS      @MRESET, @TXCSR     ;MASTER RESET
1445 004334 012777 020000 005526      MOV      @SYNEXT, @PARCSR    ;SET THE MODE
1446 004342 052777 000400 005524      BIS      @MRESET, @TXCSR     ;MASTER RESET
1447
1448                                     ;SET MAINTENANCE MODE & SEND
1449                                     ;NOTE: BIT WINDOW/CLK ARE CLEARED (MTDATA=0)
1450 004350 012777 004020 005516      MOV      @MINT!SEND, @TXCSR
1451
1452                                     ;SET MODE, # OF BITS, PARITY SENSE, & LOAD SYNC REG
1453 004356 012777 027026 005504      MOV      @SYNEXT!EIGHT!ODDPAR!26, @PARCSR
1454 004364 016703 005474                MOV      @RXDBUF, R3         ;SETUP FOR ERROR MSG
1455 004370 005004                CLR      R4                  ;FOR DATA CHAR CREATION
1456 004372 110477 005502                MOV      R4, @TXDBUF         ;LOAD CHARACTER
1457 004376 052777 000020 005454      BIS      @SYNSCH, @RXCSR     ;SET SEARCH SYNC
1458
1459                                     ;GET INTO SYNCHRONIZATION
1460 004404 052777 020000 005462      BIS      @CLK, @TXCSR        ;POKE CLK UP
1461 004412 042777 020000 005454      BIC      @CLK, @TXCSR        ;POKE CLK DOWN
1462 004420 012767 000011 174512 18:  MOV      @9, SHIFT           ;# OF SHIFTS
1463 004426 010400                MOV      R4, @0              ;EXPECTED
1464 004430
1465 004430 052777 020000 005436      BIS      @CLK, @TXCSR        ;POKE CLK UP
1466 004436 042777 020000 005430      BIC      @CLK, @TXCSR        ;POKE CLK DOWN
1467 004444 005367 174470                DEC      SHIFT              ;# OF SHIFTS
1468 004450 022767 000003 174462      CMP      @3, SHIFT           ;TIME TO LOAD NEXT CHAR ?
1469 004456 001003                BNE      @3                  ;NO ?
1470 004460 005204                INC      R4                  ;GENERATE NEXT CHAR
1471 004462 110477 005412                MOV      R4, @TXDBUF         ;LOAD NEXT CHARACTER
1472 004466 005767 174446 38:  TST      @SHIFT              ;IS IT 0 ?
1473 004472 001356                BNE      @25
1474 004474 105777 005360                TSTB    @RXCSR              ;RXDONE = 1 ?
1475 004500 100401                BMI      @55
1476 004502 104000                HLT
1477 004504                58:
1478 004504 017701 005354                MOV      @RXDBUF, R1         ;ACTUAL
1479 004510 020001                CMP      @R0, R1             ;COMPARE EXP VS ACT
1480 004512 001401                BEQ      @68
1481 004514 104002                HLT                          ;CHARACTERS SHOULD COMPARE
1482 004516                68:
1483 004516 105704                TSTB    R4                  ;LAST CHARACTER ?
1484 004520 001337                BNE      @18                ;NO
1485 004522 104400                48:  SCOPE

```



```

1485                                     :: THIS TEST PERFORMS A BINARY COUNT DATA PATTERN ON
1486                                     :: BOTH THE TRANSMITTER AND RECEIVER LOGIC
1487                                     :: MODE: SYNC EXTERNAL (SYNEXT)
1488                                     :: LENGTH: EIGHT PLUS PARITY
1489                                     :: PARITY: EVEPAR
1490                                     :: MAINT. MODE: NEXT
1491
1492 004524 012767 000007 174374 TST7: MOV      #7, TSTNO          ;SAVE THIS
1493 004532 012767 005004 174356      MOV      #TSTB, NEXT          ;GO TO THIS TEST WHEN THRU
1494 004540 105767 174437      TSTB    JMBY          ;JUMP AROUND TEST ?
1495 004544 100116      BPL     #5          ;YES ?
1496 004546 052777 000400 005320      BIS     #MRESET, #TXCSR ;MASTER RESET
1497 004554 012777 020000 005306      MOV     #SYNEXT, #PARCSR ;SET THE MODE
1498 004562 052777 000400 005304      BIS     #MRESET, #TXCSR ;MASTER RESET
1499
1500                                     ;SET MAINTENANCE MODE & SEND
1501                                     ;NOTE: BIT WINDOW & CLK ARE CLEARED (MTDATA=0)
1502 004570 012777 010020 005276      MOV     #NEXT!SEND, #TXCSR
1503
1504                                     ;SET MODE, # OF BITS, PARITY SENSE, & LOAD SYNC REG
1505 004576 012777 027426 005264      MOV     #SYNEXT!EIGHT!EVEPAR!26, #PARCSR
1506 004604 016703 005254      MOV     #RDXBUF, R3          ;SETUP FOR ERROR MSG
1507 004610 005004      CLR     R4          ;FOR DATA CHAR CREATION
1508 004612 110477 005262      MOVB   #R4, #TXDBUF        ;LOAD CHARACTER
1509 004616 052777 000020 005234      BIS     #SYNSCH, #TXCSR    ;SET SEARCH SYNC
1510                                     ;GET INTO SYNCHRONIZATION
1511 004624 052777 020000 005242      BIS     #CLK, #TXCSR       ;POKE CLK UP
1512                                     ;WAIT FOR CABLE & DRIVER DELAYS
1513 004632 016702 174300      MOV     #HOLD, R2          ;WAIT THIS AMT
1514 004636      64S:
1515 004636 005302      DEC     R2          ;WAIT
1516 004640 001376      BNE     #64S
1517                                     ;EXIT...
1518 004642 042777 020000 005224      BIC     #CLK, #TXCSR       ;POKE CLK DOWN
1519                                     ;WAIT FOR CABLE & DRIVER DELAYS
1520 004650 016702 174262      MOV     #HOLD, R2          ;WAIT THIS AMT
1521 004654      65S:
1522 004654 005302      DEC     R2          ;WAIT
1523 004656 001376      BNE     #65S
1524                                     ;EXIT...
1525 004660 012767 000011 174252 1S:  MOV     #9, #SHIFT          ;# OF SHIFTS
1526 004666 010400      MOV     #R4, R0          ;EXPECTED
1527 004670      2S:
1528 004670 052777 020000 005176      BIS     #CLK, #TXCSR       ;POKE CLK UP
1529                                     ;WAIT FOR CABLE & DRIVER DELAYS
1530 004676 016702 174234      MOV     #HOLD, R2          ;WAIT THIS AMT
1531 004702      66S:
1532 004702 005302      DEC     R2          ;WAIT
1533 004704 001376      BNE     #66S
1534                                     ;EXIT...
1535 004706 042777 020000 005160      BIC     #CLK, #TXCSR       ;POKE CLK DOWN
1536                                     ;WAIT FOR CABLE & DRIVER DELAYS
1537 004714 016702 174216      MOV     #HOLD, R2          ;WAIT THIS AMT
1538 004720      67S:
1539 004720 005302      DEC     R2          ;WAIT
1540 004722 001376      BNE     #67S
    
```

# H03

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SEQ 0033

```

1541      :EXIT...
1542      DEC      SHIFT      ;# OF SHIFTS
1543      CMP      R3,SHIFT    ;TIME TO LOAD NEXT CHAR ?
1544      BNE      35         ;NO ?
1545      INC      R4         ;GENERATE NEXT CHAR
1546      MOV     R4,DXBUF    ;LOAD NEXT CHARACTER
1547      TST     SHIFT      ;IS IT 0 ?
1548      BNE      25
1549      TST     DXCSR      ;RXDONE = 1 ?
1550      BHI      55
1551      HLT
1552      HLT      ;RXDONE SHOULD BE SET
1553      MOV     DXBUF,R1    ;ACTUAL
1554      CMP     R0,R1      ;COMPARE EXP VS ACT
1555      BEQ     65
1556      HLT      2         ;CHARACTERS SHOULD COMPARE
1557      HLT
1558      :CHECK OUT MODEM BYPASS JUMPER
1559      TST     R4         ;LAST CHARACTER ?
1560      BNE     15         ;NO
1561      SCOPE
  
```

174202

35:

55:

65:

45:

005132

005100

005074

105704

001327

104400

```

1562                                     ;; THIS TEST PERFORMS A BINARY COUNT DATA PATTERN ON
1563                                     ;; BOTH THE TRANSMITTER AND RECEIVER LOGIC
1564                                     ;; MODE: SYNC EXTERNAL (SYNEXT)
1565                                     ;; LENGTH: EIGHT PLUS PARITY
1566                                     ;; PARITY: 000PAR
1567                                     ;; MAINT. MODE: NEXT
1568
1569 005004 012767 000010 174114 TSTB: MOV      #8,TSTNO      ;SAVE THIS
1570 005012 012767 005264 174076      MOV      #.EOP,NEXT      ;GO TO THIS TEST WHEN THRU
1571 005020 105767 174157      TSTB    JMRBY      ;JUMP AROUND TEST ?
1572 005024 100116      BPL     #45      ;YES ?
1573 005026 052777 000400 005040      BIS     #MRESET,@TXCSR ;MASTER RESET
1574 005034 012777 020000 005026      MOV     #SYNEXT,@PARCSR ;SET THE MODE
1575 005042 052777 000400 005024      BIS     #MRESET,@TXCSR ;MASTER RESET
1576
1577                                     ;SET MAINTENANCE MODE & SEND
1578                                     ;NOTE: BIT WINDOW&CLK ARE CLEARED (MTDATA=0)
1579 005050 012777 010020 005016      MOV     #NEXT!SEND,@TXCSR
1580
1581                                     ;SET MODE # OF BITS, PARITY SENSE & LOAD SYNC REG
1582 005056 012777 027026 005004      MOV     #SYNEXT!EIGHT!000PAR!26,@PARCSR
1583 005064 016703 004774      MOV     R0,DBUF,R3      ;SETUP FOR ERROR MSG
1584 005070 005004      CLR     R4      ;FOR DATA CHAR CREATION
1585 005072 110477 005002      MOVB   R4,@TXDBUF      ;LOAD CHARACTER
1586 005076 052777 000020 004754      BIS     #SYNSCH,@TXCSR ;SET SEARCH SYNC
1587
1588                                     ;GET INTO SYNCHRONIZATION
1589 005104 052777 020000 004762      BIS     #CLK,@TXCSR      ;POKE CLK UP
1590                                     ;WAIT FOR CABLE & DRIVER DELAYS
1590 005112 016702 174020      MOV     HOLD,R2 ;WAIT THIS AMT
1591 005116                                     64S:
1592 005116 005302      DEC     R2      ;WAIT
1593 005120 001376      BNE     64S
1594                                     ;EXIT...
1595 005122 042777 020000 004744      BIC     #CLK,@TXCSR      ;POKE CLK DOWN
1596                                     ;WAIT FOR CABLE & DRIVER DELAYS
1597 005130 016702 174002      MOV     HOLD,R2 ;WAIT THIS AMT
1598 005134                                     65S:
1599 005134 005302      DEC     R2      ;WAIT
1600 005136 001376      BNE     65S
1601                                     ;EXIT...
1602 005140 012767 000011 173772 1S:  MOV     #9,SHIFT      ;# OF SHIFTS
1603 005146 010400      MOV     R4,R0      ;EXPECTED
1604 005150                                     2S:
1605 005150 052777 020000 004716      BIS     #CLK,@TXCSR      ;POKE CLK UP
1606                                     ;WAIT FOR CABLE & DRIVER DELAYS
1607 005156 016702 173754      MOV     HOLD,R2 ;WAIT THIS AMT
1608 005162                                     66S:
1609 005162 005302      DEC     R2      ;WAIT
1610 005164 001376      BNE     66S
1611                                     ;EXIT...
1612 005166 042777 020000 004700      BIC     #CLK,@TXCSR      ;POKE CLK DOWN
1613                                     ;WAIT FOR CABLE & DRIVER DELAYS
1614 005174 016702 173736      MOV     HOLD,R2 ;WAIT THIS AMT
1615 005200                                     67S:
1616 005200 005302      DEC     R2      ;WAIT
1617 005202 001376      BNE     67S

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# J03

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SEQ 0035

1618					:EXIT...
1619	005204	005367	173730		DEC SHIFT ;# OF SHIFTS
1620	005210	022767	000003	173722	CHP #3,SHIFT ;TIME TO LOAD NEXT CHAR ?
1621	005216	001003			BNE 3S ;NO ?
1622	005220	005204			INC R4 ;GENERATE NEXT CHAR
1623	005222	110477	004652		MOVB R4,ITXDBUF ;LOAD NEXT CHARACTER
1624	005226	005767	173706	3S:	TST SHIFT ;IS IT 0 ?
1625	005232	001346			BNE 2S
1626	005234	105777	004620		TSTB ITRXCSR ;RXDONE = 1 ?
1627	005240	100401			BMI 5S
1628	005242	104000			HLT ;RXDONE SHOULD BE SET
1629	005244			5S:	
1630	005244	017701	004614		MOV ITRXDBUF,R1 ;ACTUAL
1631	005250	020001			CHP R0,R1 ;COMPARE EXP VS ACT
1632	005252	001401			BEQ 6S
1633	005254	104002			HLT 2 ;CHARACTERS SHOULD COMPARE
1634	005256			6S:	
1635					:CHECK OUT MODEM BYPASS JUMPER
1636	005256	105704			TSTB R4 ;LAST CHARACTER ?
1637	005260	001327			BNE 1S ;NO
1638	005262	104400		4S:	SCOPE

```

1639
1640
1641
1642
1643
1644
1645
1646 005264 104402 .EOP: TYPE ;TYPE NAME OF TEST
1647 005264 010426 MEPASS
1648 005270 104410 005522 CONVRT ,OUTCRY
1649 005274 104402 010147 TYPE ,DEVICE
1650 005300 105767 173676 TSTB MULTD ;ARE YOU RUNNING MULTIPLE DEVICES ?
1651 005304 001511 BEQ CCC ;NO, JUMP AROUND
1652 005306 005767 173704 TST ACTREG ;ARE ANY DEVICES ACTIVE ?
1653 005312 001007 BNE RUNIT ;YES
1654 005314 104402 010161 TYPE ,MCON ;NO
1655 005320 016700 173672 MOV ACTREG,RO ;DISPLAY ACTREG
1656 005324 000000 HALT ;SELECT SOMETHING TO RUN @ ACTREG:
1657 ;SELECT SWITCHES & HIT CONTINUE (PUT SMOO =1)
1658 005326 000167 173726 JMP .START ;START OVER AGAIN..... YOU DESELECTED EVERYTHING
1659 005332 062767 000010 173644 RUNIT: ADD #10,BASEADD ;NEXT BLOCK (ADDRESSES)
1660 005340 062767 000010 173644 ZERO: ADD #10,BASEIV ;NEXT BLOCK (VECTORS)
1661 005346 000241 CLC
1662 005350 006167 173644 ROL ROTADD ;UP DATE ROTATING POINTER
1663 005354 103410 BCS 25 ;IS IT THE LAST DEVICE
1664 ;TO BE TESTED IN THIS PASS ?
1665 005356 036767 173636 173632 BIT ROTADD,ACTREG ;TEST THIS DEVICE FOR ACTIVE STATUS
1666 005364 001762 BEQ RUNIT ;IF NOT ACTIVE, TRY NEXT ADDRESS
1667 005366 004767 000034 JSR PC,REPLAY ;CALCULATE NEW PARAMETERS
1668 005372 000167 000174 JMP RESTRT ;YES IT WAS ACTIVE, TEST THIS DEVICE
1669 005376 012767 000001 173614 25: MOV #1,ROTADD ;OK!, NOW SET UP ROTATING
1670 ;POINTER FOR NEXT MULTIPLE PASS
1671 005404 016767 173576 173572 MOV KEEPADD,BASEADD ;RESTORE BASE ADDRESS
1672 005412 016767 173576 173572 MOV KEEPIV,BASEIV ;RESTORE BASE INTERRUPT VECTORS
1673 005420 004767 000002 JSR PC,REPLAY ;CALC NEW PARAMETERS
1674 005424 000441 BR CCC ;JUMP AROUND REPLAY
1675 005426 016767 173552 004126 REPLAY: MOV BASEADD,DUBASE ;SET UP FOR NEW ADDRESSES
1676 005434 004767 003770 JSR PC,DUADR ;CREATE NEW ADDRESSES
1677 005440 016767 173546 004436 MOV BASEIV,DURIV ;CREATE DURIV
1678 005446 062767 000002 173536 ADD #2,BASEIV
1679 005454 016767 173532 004424 MOV BASEIV,DURIS ;CREATE DURIS
1680 005462 062767 000002 173522 ADD #2,BASEIV
1681 005470 016767 173516 004412 MOV BASEIV,DUTIV ;CREATE DUTIV
1682 005476 062767 000002 173506 ADD #2,BASEIV
1683 005504 016767 173502 004400 MOV BASEIV,DUTIS ;CREATE DUTIS
1684 005512 016767 004366 173472 MOV DURIV,BASEIV ;RESTORE
1685 005520 000207 RTS PC
1686
1687 005522 000001 OUTCRY: 1
1688 005524 006 002 .BYTE 6,2
1689 005526 012060 RXCSR
1690
1691 005530 CCC:
1692 005530 005067 173400 CLR LSTERR ;CLEAR LAST ERROR PC
1693 005534 005067 173464 CLR ERRFLG ;CLEAR ERROR FLAG
1694 005540 005267 173364 INC PASCNT ;UPDATE PASS COUNT

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1695 005544 016777 173360 173330      MOV      PASCNT,ALIGHTS      ;DISPLAY PASS COUNT
1696 005552 013701 000042              MOV      @#42,R1            ;CHECK FOR ACT-11 OR DOP
1697 005556 001405              BEQ      RESTRT             ;IF NOT, CONTINUE TESTING
1698 005560 000005              RESET
1699 005562 004711              LOGICAL: JSR      PC,(R1)
1700 005564 000240              NOP
1701 005566 000240              NOP
1702 005570 000240              NOP
1703 005572 012767 000340 172176 RESTRT: MOV      @#340,PS          ;PREVENT INTERRUPTS (PRIO: 7)
1704 005600 104413              CKSMR                       ;CHECK FOR †G
1705 005602 012767 002350 173304      MOV      @TST1,RTRN
1706 005610 000167 174534              JMP      TST1
1707
1708
1709
1710 005614
1711
1712 005614 000424      .SCOPE:
;**** START OF CODE FOR THE X OR TESTER ****
1713
1714 005616 013746 000004              MOV      @#4,-(SP)          ;IF RUNNING ON THE X OR TESTER CHANGE
1715 005622 012737 005642 000004      MOV      @15,@#4           ;THIS INSTRUCTION TO A "NOP"(NOP=240)
1716 005630 005737 177060              TST      @#177060          ;SAVE CONTENTS OF ERROR VECTOR
1717 005634 012637 000004              MOV      (SP)+,@#4         ;SET FOR TIME OUT
1718 005640 000404              BR       25                ;TIME OUT ON X OR ?
1719 005642 022626              15:    CMP      (SP)+,(SP)+    ;RESTORE ERROR VECTOR
1720 005644 012637 000004              MOV      (SP)+,@#4         ;GO TO NEXT TEST
1721 005650 000403              BR       35                ;CLEAR THE STACK AFTER A TIMEOUT
1722 005652 016767 173240 173234 25:    MOV      NEXT,RTRN        ;RESTORE ERROR VECTOR
1723 005660 016716 173230 35:    MOV      RTRN,(SP)       ;LOOP ON PRESENT TEST
1724 005664 000002              RTI                        ;SET UP NEXT TEST IN RTRN
1725 005666              45:    ;**** END OF CODE FOR THE X OR TESTER ****
1726 005666 104413              CKSMR                       ;CHECK FOR †G
1727 005670 032777 040000 173202      BIT      @SW14,@SMR        ;LOOP ON CURRENT TEST ?
1728 005676 001407              TTST: BEQ      15
1729 005700 000432              BR       35
1730 005702 105777 173176              TSTB    @TKCSR            ;TEST TTY FLAG
1731 005706 100027              BPL     35
1732 005710 017700 173172              MOV     @TKDBR,R0         ;CLR DONE BIT
1733 005714 000412              BR      25                ;IF A TTY KEY IS STRUCK GO TO NEXT TST
1734 005716 032777 004000 173154 15:    BIT      @SW11,@SMR       ;INHIBIT ITERATIONS ?
1735 005724 001006              BNE     25
1736 005726 005267 173172              INC     LPCNT
1737 005732 026767 173166 173162      CMP     LPCNT,ICOUNT     ;CHECK FOR ITERATION CNT FINISH
1738 005740 101412              BLOS   35
1739 005742 105067 173256 25:    CLRB   ERRFLG
1740 005746 005067 173152              CLR    LPCNT
1741 005752 012767 000005 173142      MOV     @5,ICOUNT        ;SET UP ITERATION COUNT
1742 005760 016767 173132 173126      MOV     NEXT,RTRN        ;SET UP NEXT TEST IN RTRN
1743 005766 016716 173122 35:    MOV     RTRN,(SP)       ;SET UP STACK FOR RTI
1744 005772 000002              RTI
1745 005774 001407      BRW:   1407                ;RESTORE "BEQ 15" INSTRUCTION
1746 005776 000432      BRX:   432                ;RESTORE "BR 35" INSTRUCTION
1747
1748
1749
1750 006000 104413      .SCOPE: CKSMR            ;CHECK FOR †G
;CHECK FOR FREEZE ON CURRENT DATA

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# M03

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SEQ 0038

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1751 006002 032777 001000 173070      BIT      #SM09,SMR
1752 006010 001402                BEQ      1$
1753 006012 016716 173102                MOV      LOCK,(SP)
1754 006016 000002                RTI
1755
1756                                ;TELETYPE OUTPUT ROUTINE
1757
1758 006020 010546                .TYPE:  MOV      R5,-(SP)
1759 006022 017605 000002                MOV      @2(SP),R5
1760 006026 062766 000002 000002                ADD      #2,2(SP)
1761 006034 105715                1$:      TSTB     (R5)                ;LOOK FOR "0"
1762 006036 001406                BEQ      3$
1763 006040 105777 173044                2$:      TSTB     @TPCSR                ;TEST DONE BIT
1764 006044 100375                BPL      2$
1765 006046 112577 173040                MOVB    (R5)+,@TPDBR                ;TYPE CHAR
1766 006052 000770                BR       1$                ;DO IT AGAIN UNTIL "0" IS SEEN
1767 006054 012605                3$:      MOV      (SP)+,R5
1768 006056 000002                RTI
1769
1770                                ;ASCII STRING INPUT ROUTINE
1771
1772 006060 010346                .INSTR: MOV      R3,-(SP)
1773 006062 010446                MOV      R4,-(SP)
1774 006064 017667 000004 000010                MOV      @4(SP),MSG
1775 006072 062766 000002 000004                ADD      #2,4(SP)
1776 006100 104402                .INST1: TYPE
1777 006102 000000                .MSG:   0
1778 006104 012704 011214                MOV      @INBUF,R4
1779 006110 012703 000007                MOV      #7,R3
1780 006114 105777 172764                1$:      TSTB     @TKCSR
1781 006120 100375                BPL      1$
1782 006122 117714 172760                MOVB    @TKDBR,(R4)
1783 006126 142714 000200                BICB    #200,(R4)
1784 006132 121427 000025                CMPB    (R4),#25                ;IS IT <10>
1785 006136 001003                BNE     200$
1786 006140 104402 010336                TYPE,MCRLF
1787 006144 000755                BR       .INST1
1788 006146 122427 000015                200$:   CMPB    (R4)+,#15
1789 006152 001423                BEQ     INSTR2
1790 006154 117777 172726 172730                MOVB    @TKDBR,@TPDBR
1791 006162 105777 172722                2$:      TSTB     @TPCSR
1792 006166 100375                BPL      2$
1793 006170 005303                DEC     R3
1794 006172 001350                BNE     1$
1795 006174 000402                BR       .INSTG
1796 006176 010346                .INSTE: MOV      R3,-(SP)
1797 006200 010446                .INSTG: MOV      R4,-(SP)
1798 006202 104402                .INSTG: TYPE
1799 006204 010332                MCM
1800 006206 005737 007474                TST     @RDSW
1801 006212 001402                BEQ     400$
1802 006214 104402 010336                TYPE,MCRLF
1803 006220 000727                400$:   BR       .INST1
1804 006222 012604                INSTR2: MOV      (SP)+,R4
1805 006224 012603                MOV      (SP)+,R3
1806 006226 000002                RTI
  
```

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1807
1808
1809
1810 006230 010546
1811 006232 010446
1812 006234 016605 000004
1813 006240 012567 000170
1814 006244 012567 000166
1815 006250 012567 000164
1816 006254 112567 000162
1817 006260 112567 000157
1818 006264 010566 000004
1819 006270 005005
1820 006272 012704 011214
1821 006276 122714 000015
1822 006302 001420
1823 006304 121427 000060
1824 006310 002415
1825 006312 121427 000067
1826 006316 003012
1827 006320 142714 000060
1828 006324 152405
1829 006326 122714 000015
1830 006332 001414
1831 006334 006305
1832 006336 006305
1833 006340 006305
1834 006342 000760
1835 006344 122714 000015
1836 006350 001003
1837 006352 005737 007474
1838 006356 001023
1839 006360 104404
1840 006362 000742
1841
1842
1843
1844 006364 020567 000046
1845 006370 101365
1846 006372 020567 000036
1847 006376 103762
1848 006400 136705 000036
1849 006404 001357
1850
1851
1852
1853 006406 016704 000026
1854 006412 010524
1855 006414 062705 000002
1856 006420 105367 000017
1857 006424 001372
1858 006426 012604
1859 006430 012605
1860 006432 000002
1861 006434 000000
1862 006436 000000
    
```

```

;CONVERT ASCII STRING TO OCTAL
.PARAM: MOV R5, -(SP)
        MOV R4, -(SP)
        MOV 4(SP), R5
        MOV (R5)+, LOLIM
        MOV (R5)+, HILIM
        MOV (R5)+, DEVAOR
        MOV (R5)+, LOBITS
        MOV (R5)+, ADRCNT
        MOV R5, 4(SP)
PARAM1: CLR R5
        MOV #INBUF, R4
        CMPB #15, (R4)
        BEQ PARERR
1S:     CMPB (R4), #60
        BLT PARERR
        CMPB (R4), #67
        BGT PARERR
        BICB #60, (R4)
        BISB (R4)+, R5
        CMPB #15, (R4)
        BEQ LIMITS
        ASL R5
        ASL R5
        ASL R5
        BR 1S
PARERR: CMPB #15, (R4) ;IS FIRST CHARACTER A <CR>
        BNE 120S
        TST #ARDSM ;IS CKSMR ROUTINE BEING USED
        BNE PARTI
120S:   INSTER
        BR PARAM1

;TEST TO SEE IF NUMBER IS WITHIN LIMITS
LIMITS: CMP R5, HILIM
        BHI PARERR
        CMP R5, LOLIM
        BLO PARERR
        BITB LOBITS, R5
        BNE PARERR

;STORE NUMBER AT SPECIFIED ADDRESS
1S:     MOV DEVAOR, R4
        MOV R5, (R4)+
        ADD #2, R5
        DECB ADRCNT
        BNE 1S
PARTI:  MOV (SP)+, R4
        MOV (SP)+, R5
        RTI
LOLIM:  0
HILIM:  0
    
```



```

1863 006440 000000          DEVADR: 0
1864 006442 000000          LOBITS: 0
1865          006443          ADRCNT=LOBITS+1
1866
1867          ;SAVE PC OF TEST THAT FAILED AND RO-RS
1868
1869 006444 016667 000004 172522 .SAV05: MOV    4(SP),SAVPC
1870
1871          ;SAVE RO-RS
1872
1873 006452 010567 172512          SV05:  MOV    R5,SAVR5
1874 006456 010467 172504          MOV    R4,SAVR4
1875 006462 010367 172476          MOV    R3,SAVR3
1876 006466 010267 172470          MOV    R2,SAVR2
1877 006472 010167 172462          MOV    R1,SAVR1
1878 006476 010067 172454          MOV    R0,SAVR0
1879 006502 000002          RTI
1880
1881          ;RESTORE RO-RS
1882
1883 006504 016700 172446          .RES05: MOV    SAVR0,R0
1884 006510 016701 172444          MOV    SAVR1,R1
1885 006514 016702 172442          MOV    SAVR2,R2
1886 006520 016703 172440          MOV    SAVR3,R3
1887 006524 016704 172436          MOV    SAVR4,R4
1888 006530 016705 172434          MOV    SAVR5,R5
1889 006534 000002          RTI
1890
1891          ;CONVERT OCTAL NUMBER TO ASCII AND OUTPUT TO TELEPRINTER
1892
1893 006536 104402          .CONVR: TYPE
1894 006540 010336          MCR LF
1895 006542 010046          .CNVRT: MOV    R0,-(SP)
1896 006544 010146          MOV    R1,-(SP)
1897 006546 010346          MOV    R3,-(SP)
1898 006550 010446          MOV    R4,-(SP)
1899 006552 010546          MOV    R5,-(SP)
1900 006554 017601 000012          MOV    2(12(SP),R1
1901 006560 016767 002470          MOV    TEMP,TEMP3
1902 006566 062766 000002          ADD    #2,12(SP)
1903 006574 012167 000154          MOV    (R1)+,WRDCNT
1904 006600 112167 000152          15:  MOVB  (R1)+,CHRCNT
1905 006604 112167 000147          MOVB  (R1)+,SPACNT
1906 006610 013167 000144          MOV    2(R1)+,BINWRD
1907 006614 016704 000140          25:  MOV    BINWRD,R4
1908 006620 116705 000132          MOVB  CHRCNT,R5
1909 006624 012700 011254          MOV    #TEMP,R0
1910 006630 010403          35:  MOV    R4,R3
1911 006632 042703 177770          BIC    #177770,R3
1912 006636 062703 000060          ADD    #060,R3
1913 006642 110320          MOVB  R3,(R0)+
1914 006644 006204          ASR   R4
1915 006646 042704 100000          BIC   #100000,R4
1916 006652 006204          ASR   R4
1917 006654 006204          ASR   R4
1918 006656 005305          DEC   R5
    
```

```

;SHIFT FOR NEXT #
;CLUGE TO STOP BIT 15 PROPAGATING.
;DITTO
;DITTO
    
```

```

1919 006660 001363
1920 006662 012703 011314
1921 006666 114023
1922 006670 105367 000062
1923 006674 001374
1924 006676 105767 000055
1925 006702 001405
1926 006704 112723 000040
1927 006710 105367 000043
1928 006714 001373
1929 006716 105013
1930 006720 104402
1931 006722 011314
1932 006724 005367 000024
1933 006730 001323
1934 006732 016767 172212 002314
1935 006740 012605
1936 006742 012604
1937 006744 012603
1938 006746 012601
1939 006750 012600
1940 006752 000002
1941 006754 000000
1942 006756 000000
1943 006757 000000
1944 006760 000000
1945
1946
1947
1948
1949
1950
1951 006762 017605 000000
1952 006766 122767 000116 002220
1953 006774 001002
1954 006776 105015
1955 007000 000406
1956 007002 122767 000131 002204
1957 007010 001005
1958 007012 112715 177777
1959 007016 062716 000002
1960 007022 000002
1961 007024 104404
1962 007026 000755
1963
1964
1965
1966
1967
1968 007030 011646
1969 007032 162716 000002
1970 007036 017616 000000
1971 007042 006316
1972 007044 042716 177001
1973 007050 062716 001226
1974 007054 017616 000000

```

```

BNE 35
MOV #MDATA,R3
4S: MOV# -(R0),(R3)+
DECB CHRCNT
BNE 4S
TSTB SPACNT
BEQ 6S
5S: MOV# #040,(R3)+
DECB SPACNT
BNE 5S
6S: CLRB (R3)
TYPE
MDATA
DEC WRCNT
BNE 1S
MOV TEMP3,TEMP
MOV (SP)+,R5
MOV (SP)+,R4
MOV (SP)+,R3
MOV (SP)+,R1
MOV (SP)+,R0
RTI
WRCNT: 0
CHRCNT: 0
SPACNT=CHRCNT+1
BINWRD: 0

```

```

;COMPARE THE FIRST CHARACTER IN THE TELETYPE INPUT
;BUFFER TO THE CHARACTERS "N" AND "Y"
;IF THE CHARACTER IS "N" CLEAR THE FLAG
;IF THE CHARACTER IS "Y" SET THE FLAG

```

```

.SETFLG: MOV 0(SP),R5
CMPB 0(N,INBUF) ;IS IT "N" ?
BNE 1S
CLRB (R5) ;000
BR 2S
1S: CMPB 0(Y,INBUF) ;IS IT "Y" ?
BNE 3S
MOV# 0-1,(R5) ;377
2S: ADD 02,(SP)
RTI
3S: INSTER ;RETRY
BR .SETFLG

```

```

;TRAP DISPATCH SERVICE
;ARGUMENT OF TRAP IS EXTRACTED
;AND USED AS OFFSET TO OBTAIN POINTER
;TO SELECTED SUBROUTINE

```

```

.TRPSR: MOV (SP)-,(SP) ;GET PC OF RETURN
SUB 02,(SP) ;PC OF TRAP
MOV 0(SP),(SP) ;GET TRAP
TRPOK: RSL (SP) ;MULTIPLY TRAP ARG BY 2
BIC 0177001,(SP) ;CLEAR UNWANTED BITS
ADD 0,TRPTAB,(SP) ;POINTER TO SUBROUTINE ADDRESS
MOV 0(SP),(SP) ;SUBROUTINE ADDRESS

```

```

1975 007060 000136          JMP      2(SP)+          ;GO TO SUBROUTINE
1976
1977          ;ERROR HANDLER
1978
1979 007062 104413          .HLT:  CKSMR          ;CHECK FOR IG
1980 007064 032777 020000 172006  BIT      @SM13,@SMR    ;INHIBIT ERROR TYPE OUT ?
1981 007072 001061          BNE     HALTS
1982 007074 021667 172034  CNP     (SP),LSTERR
1983 007100 001404          BEQ     IS
1984 007102 011667 172026  MOV     (SP),LSTERR
1985 007106 105067 172112  CLRB   ERRFLG
1986 007112 104406          IS:    SAVOS
1987 007114 011605          MOV     (SP),R5
1988 007116 162705 000002  SUB     @2,R5
1989 007122 011504          MOV     (R5),R4
1990 007124 006304          ASL    R4
1991 007126 061504          ADD    (R5),R4
1992 007130 006304          ASL    R4
1993 007132 042704 177001  BIC    @177001,R4
1994 007136 062704 012030  ADD    @,ERRTAB,R4
1995 007142 012467 000040  MOV    (R4)+,ERRMSG
1996 007146 012467 000046  MOV    (R4)+,DATAHD
1997 007152 011467 000054  MOV    (R4),DATABP
1998 007156 105767 172042  TSTB   ERRFLG
1999 007162 001403          BEQ    TYPMSG
2000 007164 005767 000042  TST    DATABP
2001 007170 001014          BNE    TYPDAT
2002 007172 104410          TYPMSG: CONVRT
2003 007174 007324          ERTAB0
2004 007176 112767 177777 172020  MOVB   @-1,ERRFLG
2005 007204 104402          TYPE
2006 007206 000000          ERRMSG: 0
2007 007210 005767 000004  TST    DATAHD
2008 007214 001402          BEQ    TYPDAT
2009 007216 104402          TYPE
2010 007220 000000          DATAHD: 0
2011 007222 005767 000004  TST    DATABP
2012 007226 001402          BEQ    RESREG
2013 007230 104410          CONVRT
2014 007232 000000          DATABP: 0
2015 007234 104407          RESREG: RESOS
2016 007236 005777 171636  HALTS:  TST    @SMR
2017 007242 100005          BPL    EXITER
2018 007244 010046          PUSHRO
2019 007246 016600 000002  MOV    2(SP),R0
2020 007252 000000          HALT
2021 007254 012500          POPRO
2022 007256 104413          EXITER: CKSMR
2023 007260 005267 171646          INC    ERRCNT          ;CHECK FOR IG
2024 007264 032777 000400 171606  BIT    @SM08,@SMR    ;LOOP ON ERROR ?
2025 007272 001007          BNE    IS
2026 007274 032777 002000 171576  BIT    @SM10,@SMR    ;ESCAPE TO NEXT ON ERROR ?
2027 007302 001407          BEQ    @25
2028 007304 016767 171606 171602  MOV    NEXT,RTN
2029 007312 012706 001100          IS:    MOV    @STACK,SP
2030 007316 000177 171572          JMP    @RTN          ;REINITIALIZE SP

```

```

2031 007322 000002          24: RTI
2032 007324 000001          LRTAB0: 1
2033 007326 006          .BYTE 6,2
2034 007330 001174          SAVPC
;ENTER HERE ON POWER FAILURE

2035
2036
2037
2038 007332 010046          .PFAIL: MOV R0,-(SP) ;SAVE R0-R5 ON PROCESSOR STACK
2039 007334 010146          NOV R1,-(SP)
2040 007336 010246          NOV R2,-(SP)
2041 007340 010346          NOV R3,-(SP)
2042 007342 010446          NOV R4,-(SP)
2043 007344 010546          NOV R5,-(SP)
2044 007346 016746 170452          NOV R24,-(SP)
2045 007352 010667 171614          NOV SP,SAVSP ;SAVE STACK POINTER
2046 007356 012767 007370 170440          NOV #RESTART,24 ;SET UP FOR POWER UP TRAP
2047 007364 000000          HALT ;HALT ON POWER DOWN NORMAL
2048 007366 000777          IS: BR IS
;PROCESSOR WILL TRAP HERE WHEN POWER IS RESTORED

2049
2050
2051
2052
2053 007370 016706 171576          RESTAR: MOV SAVSP,SP ;RESTORE STACK POINTER
2054 007374 012605          NOV (SP)+,R5 ;RESTORE R0-R5
2055 007376 012604          NOV (SP)+,R4
2056 007400 012603          NOV (SP)+,R3
2057 007402 012602          NOV (SP)+,R2
2058 007404 012601          NOV (SP)+,R1
2059 007406 012600          NOV (SP)+,R0
2060 007410 012767 007332 170406          NOV #.PFAIL,24 ;SET UP FOR POWER FAILURE
2061 007416 012767 000340 170352          NOV #340,PS
2062 007424 012706 001100          NOV #STACK,SP
2063 007430 005067 001620          CLR TEMP
2064 007434 005267 001614          IS: INC TEMP
2065 007440 001375          BNE IS
2066 007442 104410          CONVRT
2067 007444 007466          PFTAB
2068 007446 104402          TYPE
2069 007450 010341          MPFAIL
2070 007452 005067 171546          CLR ERRFLG
2071 007454 005067 171452          CLR LSTERR
2072 007456 000177 171426          JHP #RTRN
2073 007466 000001          PFTAB: 1
2074 007470 006          .BYTE 6,2
2075 007472 001114          RTRN

2076
2077 ;CHECK SWITCH REGISTER ROUTINE. CHECKS FOR 16 TO ALLOW CHANGING
2078 ;OF LOC. 176.
2079 ;LOCATIONS USED:
2080 007474 000000          RDSW: .WORD 0
2081
2082
2083 007476 005737 000042          .CKSMR: TST #42
2084 007502 001042          BNE OUT
2085 007504 022767 000176 171366          CMP #SMREG,SMR ;SOFTWARE SWITCH REGISTER PRESENT
2086 007512 001036          BNE OUT ;NO, GET OUT

```



```

2143 010110 053111 051105 041440
2144 010116 047117 051124 046117
2145 010124 051040 043505 051511
2146 010130 042524 020122 042101
2147 010140 051104 051505 026523
2148 010146 000
2149 010147 075 042504 044526
2150 010154 042503 020040 000
2151 010161 015 044012 053517
2152 010166 047040 053517 041040
2153 010174 047522 047127 041440
2154 010200 053517 020077 027056
2155 010210 051456 046105 041506
2156 010216 020124 047523 042515
2157 010224 044124 047111 020107
2158 010230 047524 051040 047126
2159 010240 040040 041501 051124
2160 010246 043505 000
2161 010251 015 047412 052126
2162 010256 047440 020106 040522
2163 010257 043516 035105 042523
2164 010272 054524 042520 046040
2165 010300 051501 020124 042504
2166 010306 044526 042503 051040
2167 010314 041530 051123 040440
2168 010322 042104 042522 051523
2169 010330 000055
2170 010330 020040 000077
2171 010336 005015 000
2172 010341 040 050040 053517
2173 010346 051105 043040 044501
2174 010354 052514 042522 020054
2175 010362 051120 043517 040522
2176 010370 020115 042524 052123
2177 010376 051101 020124 052101
2178 010404 052040 051505 020124
2179 010412 047111 050040 047523
2180 010420 051107 051505 000123
2181 010426 005016 047105 020104
2182 010434 043117 050040 051501
2183 010442 020123 040524 042520
2184 010448 043040 000
2185 010453 015 051012 000
2186 010457 015 052012 051505
2187 010464 020124 041520 000055
2188 010472 005015 047514 045503
2189 010500 047440 020116 042523
2190 010506 042514 052103 042106
2191 010514 052040 051505 037524
2192 010517 024040 020131 051117
2193 010520 047040 026451 000
2194 010523 015 042012 020125
2195 010524 051120 047511 044522
2196 010550 054524 046040 053105
2197 010556 046105 000055
2198 010562 005015 020043 043117

```

```

DEVICE: .ASCIZ /=DEVICE /
MCOM: .ASCIZ <15><12>/HOW NOW BROWN COW? ...SELECT SOMETHING TO RUN @ACTREG/
NRANGE: .ASCIZ <15><12>/OUT OF RANGE:RETYPE LAST DEVICE RXCSR ADDRESS-/
MOM: .ASCIZ / ?/
MCRLF: .ASCIZ <15><12>
MPFAIL: .ASCIZ / POWER FAILURE, PROGRAM RESTART AT TEST IN PROGRESS/
MEPASS: .ASCIZ <15><12>/END OF PASS TAPE F/
MR: .ASCIZ <15><12>/R/
MTSTPC: .ASCIZ <15><12>/TEST PC-/
MLOCK: .ASCIZ <15><12>/LOCK ON SELECTED TEST? (Y OR N)-/
MLEVEL: .ASCIZ <15><12>/DU PRIORITY LEVEL-/
MSYNC: .ASCIZ <15><12>/# OF SYNC CHARS SELECTED ( 1 OR 2)-/

```

```

2199 010570 051440 047131 020103
2200 010576 044103 051101 020123
2201 010604 042523 042514 052103
2202 010612 042105 024040 030440
2203 010620 047440 020122 024462
2204 010630 000055
2205 010638 005015 051511 051440
2206 010646 041505 054040 044515
2207 010654 020124 052512 050115
2208 010662 051105 021440 020066
2209 010670 047111 020077 054450
2210 010678 047440 020122 024516
2211 010686 000055
2212 010694 005015 051511 051440
2213 010702 041505 051040 041505
2214 010710 045040 046525 042520
2215 010718 020122 032443 044440
2216 010726 037516 024040 020131
2217 010734 051117 047040 026451
2218 010742 000
2219 010750 015 044412 020123
2220 010758 050117 020124 046103
2221 010766 020122 047105 041101
2222 010774 042514 045040 046525
2223 010782 042520 020122 032043
2224 011000 044440 037516 024040
2225 011008 020131 051117 047040
2226 011016 026451 000
2227 011024 015 040412 042522
2228 011032 054440 052517 051040
2229 011040 047120 044516 043516
2230 011048 044440 020116 040515
2231 011056 047111 020124 047515
2232 011064 042504 042440 052130
2233 011072 051105 040516 037514
2234 011080 005015 040401 042116
2235 011088 027040 027056 027056
2236 011096 042040 020117 047531
2237 011104 020126 040510 042526
2238 011112 052040 042510 042440
2239 011120 052130 051105 040516
2240 011128 020114 047516 042504
2241 011136 020115 054502 040520
2242 011144 051523
2243 011152 005015 045001 046525
2244 011160 042520 020122 047503
2245 011168 047116 041505 047524
2246 011176 020122 047117 037440
2247 011202 054450 047440 020122
2248 011210 024516 000055

```

MWIRE6: .ASCIZ <15><12>/IS SEC XMIT JUMPER #6 IN? (Y OR N)-/

MWIRE5: .ASCIZ <15><12>/IS SEC REC JUMPER #5 IN? (Y OR N)-/

MWIRE4: .ASCIZ <15><12>/IS OPT CLR ENABLE JUMPER #4 IN? (Y OR N)-/

NEXTJ: .ASCII <15><12>/ARE YOU RUNNING IN MAINT MODE EXTERNAL?/

.ASCII <15><12><1>/AND ..... DO YOU HAVE THE EXTERNAL MODEM BYPASS/

.ASCIZ <15><12><1>/JUMPER CONNECTOR ON?(Y OR N)-/

.EVEN

;BUFFERS FOR INPUT-OUTPUT

INBUF: .BLKB 40  
TEMP: .BLKB 40

2295  
2296  
2297  
2298  
2299  
2300  
2301  
2302  
2303  
2304  
2305  
2306  
2307  
2308  
2309  
2310

011314 000040  
  
011354 006367 000044  
011360 006367 000040  
011364 006367 000034  
011370 006367 000030  
011374 006367 000024  
011400 016767 000020 000020  
011406 162767 000001 000012  
011414 042767 000037 000004  
011422 000207  
011424 000240  
011426 000200  
  
011430 016767 000126 000422  
011436 005267 000120  
011442 016767 000114 000412  
011450 005267 000106  
011454 016767 000102 000402  
011462 016767 000074 000400  
011470 005267 000066  
011474 016767 000062 000364  
011502 016767 000054 000362  
011510 005267 000046  
011514 016767 000042 000352  
011522 005267 000034  
011526 016767 000030 000342  
011534 005267 000022  
011540 016767 000016 000332  
011546 005267 000010  
011552 016767 000004 000322  
011560 000207  
011562 000000  
  
011564 042777 040000 000302  
011572 005067 167350  
011576 006067 167342  
011602 006067 167340  
011606 006267 167334  
011612 042767 100000 167326  
011620 056777 167322 000246  
011626 042777 020000 000240  
011634 052777 020000 000232  
011642 005367 167272  
011646 001346  
011650 000207

```
MDATA: .BLKB 40
;*****
;UTILITIES
;*****

;THIS UTILITY CALCULATES PRIORITY LEVEL
DULEV: ASL     DUPRT     ;SHIFT LEFT
        ASL     DUPRT
        ASL     DUPRT
        ASL     DUPRT
        ASL     DUPRT
        MOV     DUPRT,LESS1 ;MOVE THIS TO LESS1
        SUB     #1,LESS1   ;CREATE LESS1
        BIC     #37,LESS1  ;CLEAR TNZVC
        RTS
        PC

DUPRT:  LEVEL5
LESS1:  LEVEL4 ;LEVEL TO ALLOW INTERRUPTS

;NEW DU ADDRESSES
DURDDR: MOV     DUBASE,RXCSR ;XXX0
        INC     DUBASE
        MOV     DUBASE,HRXCSR ;XXX1
        INC     DUBASE
        MOV     DUBASE,RXDBUF ;XXX2
        MOV     DUBASE,PARCSR ;XXX2
        INC     DUBASE
        MOV     DUBASE,HRXDBUF ;XXX3
        MOV     DUBASE,HPARCSR ;XXX3
        INC     DUBASE
        MOV     DUBASE,TXCSR ;XXX4
        INC     DUBASE
        MOV     DUBASE,HTXCSR ;XXX5
        INC     DUBASE
        MOV     DUBASE,TXDBUF ;XXX6
        INC     DUBASE
        MOV     DUBASE,HTXDBUF ;XXX7
        RTS
        PC

DUBASE: 0

;THIS UTILITY POKES THE MAINT DATA BASED UPON THE
;INFORMATION CONTAINED IN TEMP1 AND IT IS
;SHIFTED IN BY THE CONTENTS OF SHIFT
RPOKE:  BIC     @MDATA,@TXCSR
        CLR     TEMP2
        ROR     TEMP1 ;FORCE CARRY
        ROR     TEMP2 ;PICK UP CARRY IN BIT 15
        ASR     TEMP2 ;SHIFT INTO BIT 14
        BIC     @BIT15,TEMP2 ;CLR BIT 15
        BIS     TEMP2,@TXCSR ;POKE MAINT DATA
        BIC     @CLK,@TXCSR ;POKE CLK
        BIS     @CLK,@TXCSR
        DEC     SHIFT
        BNE     RPOKE
        RTS
        PC
```

;THIS ROUTINE CALCULATES ODD PARITY FOR AN 8 BIT CHAR



```

2311 011652 016767 167266 167266 0008:  MOV    TEMP1,TEMP2    ;SAVE TEMP1
2312 011660 005067 167264          CLR    TEMP3
2313 011664 012727 000010          MOV    #8.,(PC)+
2314 011670 000000          1S:   0
2315 011672 006067 167250          2S:   ROR    TEMP2
2316 011676 005567 167246          ADC    TEMP3
2317 011702 005367 177762          DEC    1S
2318 011706 001371          BNE    2S
2319 011710 006067 167234          ROR    TEMP3
2320 011714 103404          BCS    3S
2321 011716 052767 000400 167220  BIS    #BIT8,TEMP1    ;SET ODD PARITY
2322 011724 000400          BR     3S
2323 011726 042767 000400 167210 3S:   BIC    #BIT8,TEMP1    ;CLR EVEN PARITY
2324          :TEMP1 NOW HAS ODD PARITY CHARACTER
2325 011734 000207          4S:   RTS    PC
2326          :THIS ROUTINE CALCULATES EVEN PARITY FOR AN 8 BIT CHARACTER
2327          EVEN8:
2328 011736 016767 167202 167202  MOV    TEMP1,TEMP2    ;SAVE TEMP1
2329 011744 005067 167200          CLR    TEMP3
2330 011750 012727 000010          MOV    #8.,(PC)+
2331 011754 000000          1S:   0
2332 011756 006067 167164          2S:   ROR    TEMP2
2333 011762 005567 167162          ADC    TEMP3
2334 011766 005367 177762          DEC    1S
2335 011772 001371          BNE    2S
2336 011774 006067 167150          ROR    TEMP3
2337 012000 103004          BCC    3S
2338 012002 052767 000400 167134  BIS    #BIT8,TEMP1    ;SET EVEN PARITY
2339 012010 000403          BR     4S
2340 012012 042767 000400 167124 3S:   BIC    #BIT8,TEMP1    ;CLR ODD PARITY
2341          :TEMP1 NOW HAS EVEN PARITY CHARACTER
2342 012020 000207          4S:   RTS    PC
2343 012022 062716 000002  TRPREG: ADD    #2,(SP) ;ALLOW IT TO "CRUNCH" INTO HLT BACK
2344          ;IN MAIN PART OF THE PROGRAM
2345 012026 000002          RTI
2346          ;ERROR HLT TABLE
2347 012030 012114          .ERRTAB:  EMO    ;HLT 0 BIT ERROR (GENERAL)
2348 012032 000000          0
2349 012034 000000          0
2350 012036 012130          EM1    ;HLT 1 REGISTER ERROR
2351 012040 012301          DH1
2352 012042 012322          DT1
2353 012044 012172          EM2    ;HLT 2 RECEIVER ERROR
2354 012046 012301          DH1
2355 012050 012322          DT1
2356 012052 012234          EM3    ;HLT 3 TRANSMITTER ERROR
2357 012054 012301          DH1
2358 012056 012322          DT1
2359          :DEFAULT DU ADDRESSES
2360 012060 160040          RXCSR: 160040
2361 012062 160041          HRXCSR:160041
2362 012064 160042          RXDBUF:160042
2363 012066 160043          HRXDBUF:160043
2364 012070 160042          PARCSR:160042
2365 012072 160043          HPARCSR:160043
2366 012074 160044          TXCSR: 160044

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2367 012076 160045
2368 012100 160046
2369 012102 160047
2370
2371 012104 000770
2372 012106 000772
2373 012110 000774
2374 012112 000776
2375
2376 012114 036440 042440 051122
2377 012122 051117 050040 000103
2378 012130 036440 051040 043505
2379 012136 051511 042524 020122
2380 012144 051105 047522 020122
2381 012152 041520 005015 051001
2382 012160 043505 051511 042524
2383 012166 020122 000040
2384 012172 036440 051040 041505
2385 012200 044505 042526 020122
2386 012206 051105 047522 020122
2387 012214 041520 005015 051001
2388 012222 043505 051511 042524
2389 012230 020122 000040
2390 012234 036440 052040 040522
2391 012242 051516 044515 052124
2392 012250 051105 042440 051122
2393 012256 051117 050040 006503
2394 012264 000412 042522 044507
2395 012272 052123 051105 020040
2396 012300 000
2397
2398 012301 105 050130 041505
2399 012306 042524 020104 040440
400 012314 052103 040525 000114
401
402
403 012322 000003
404 012324 006 004
405 012326 001164
406 012330 006 004
407 012332 001156
408 012334 006 002
409 012336 001160
410 000001

```

```

HTXCSR: 160045
TXDBUF: 160046
HTXDBUF: 160047
:DEFAULT DU VECTORS
DURIV: 770 :REC INTR VECTOR
DURIS: 772 :REC INTR STATUS
DUTIV: 774 :XMIT INTR VECTOR
DUTIS: 776 :XMIT INTR STATUS
:ERROR MESSAGES
EMO: .ASCIZ / = ERROR PC/
EM1: .ASCIZ / = REGISTER ERROR PC/<15><12><1>/REGISTER /
EM2: .ASCIZ / = RECEIVER ERROR PC/<15><12><1>/REGISTER /
EM3: .ASCIZ / = TRANSMITTER ERROR PC/<15><12><1>/REGISTER /
:DATA HEADERS FOR ERROR MESSAGES
DH1: .ASCIZ /EXPECTED ACTUAL/
.EVEN
DT1: :DATA TABLES FOR ERROR MESSAGES
3
:BYTE 6,4
SAVR3 :REGISTER
:BYTE 6,4
SAVR0 :EXPECTED DATA
:BYTE 6,2
SAVR1 :ACTUAL DATA
.END

```







CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0053

PS = 177776	601#	828#	987#	1703#	2060#															
PUSHRO = 010046	608#	2018																		
PUSHIS = 005746	606#																			
PUSH2S = 024646	610#																			
RDSM = 007474	1800	1837	2080#	2094#	2104#															
REACT = 004000	647#	1056	1090	1146	1170	1236	1260	1326	1350											
REPLAY = 005426	1667	1673	1675#																	
RESREG = 007234	2012	2015#																		
RESTAR = 007370	2046	2052#																		
RESTRT = 005572	1668	1697	1703#																	
RESOS = 104407	806#	2015																		
RING = 040000	644#																			
RINTEN = 000100	652#																			
ROTADD = 001220	775#	903#	915#	917	919#	924	927#	1662#	1665	1669#										
RPOKE = 011564	2297#	2307																		
RTRN = 001114	729#	838#	1017	1021#	1023	1705#	1722#	1723	1742#	1743	2028#	2030	2071							
	2074																			
RTS = 000004	656#																			
RUNA = #####	1	33	45	108	546															
RLNB = #####	1	33	45	108	546															
RLNC = #####	1	33	45	108	546															
RLND = #####	1	33	45	108	546															
RLNE = #####	1	33	45	108	546															
RLNF = #####	1	33	45	108	546															
RUNIT = 005332	1653	1659#	1666																	
ROCSR = 012060	1047#	1056	1080	1107	1137#	1146	1170	1197	1227#	1236	1260	1287	1317#							
	1326	1350	1377	1407#	1423	1457#	1473	1509#	1549	1586#	1626	1689	2274#							
	2360#																			
RODBUF = 012064	1042	1086	1132	1176	1222	1266	1312	1356	1404	1427	1454	1477	1506							
	1553	1583	1630	2278#	2362#															
RODONE = 000200	651#																			
ROERR = 100000	660#																			
SAVPC = 001174	756#	1869#	2034																	
SAVR0 = 001156	749#	1878#	1883	2407																
SAVR1 = 001160	750#	1877#	1894	2409																
SAVR2 = 001162	751#	1876#	1895																	
SAVR3 = 001164	752#	1875#	1896	2405																
SAVR4 = 001166	753#	1874#	1897																	
SAVR5 = 001170	754#	1873#	1898																	
SAVSP = 001172	755#	2045#	2052																	
SAVOS = 104406	804#	1986																		
SCOPE = 104400	792#	1114	1204	1294	1384	1434	1484	1561	1638											
SCOPI = 104401	794#	1096	1186	1276	1366															
SEND = 000020	687#	1045	1135	1225	1315	1400	1450	1502	1579											
SEREC = 001200	760#	975																		
SETFLG = 104412	812#	897	970	974	978	982	1001													
SEVEN = 004000	673#	1224																		
SEXMIT = 001177	759#	971																		
SHIFT = 001140	742#	1069#	1073#	1159#	1163#	1249#	1253#	1339#	1343#	1411#	1416#	1417	1421							
	1461#	1466#	1467	1471	1525#	1542#	1543	1547	1602#	1619#	1620	1624	2306#							
SIX = 002000	672#	1134																		
SPACNT = 006757	1905#	1924	1927#	1943#																
SRO = 002000	648#																			
STACK = 001100	602#	829	988	2029	2061															
STD = 000010	655#																			
STFLG = 001223	781#	832#																		

UUUUUU









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DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 62  
DZDUF.C.P11 05-AUG-76 00:00 CROSS REFERENCE TABLE -- MACRO NAMES

SEQ 0057

SWORF 5528  
SWORD 5528  
SWORP 5528

. ABS. 012340 000

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

DZDUF.C,DZDUF.C/CRF/SOL=HELLO.P11,PARA.P11,KEET.P11,DZDUF.C.P11  
RUN-TIME: 16 23 2 SECONDS  
RUN-TIME RATIO: 117/42=2.7  
CORE USED: 18K (35 PAGES)

